Surfactants and digital alloys for strain relief in III-nitride distributed Bragg reflectors and related heterostructures via metal organic vapor phase epitaxy

Lee Ellen Rodak
West Virginia University

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SURFACTANTS AND DIGITAL ALLOYS FOR STRAIN RELIEF IN III-NITRIDE DISTRIBUTED BRAGG REFLECTORS AND RELATED HETEROSTRUCTURES VIA METAL ORGANIC VAPOR PHASE EPITAXY

by

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Dissertation submitted to the College of Engineering and Mineral Resources at West Virginia University in partial fulfillment of the requirements for the degree of

Doctor of Philosophy in Electrical Engineering

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2011

Keywords: Distributed Bragg Reflectors, Metal Organic Vapor Phase Epitaxy, Indium Surfactant, Digital Alloy

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A B S T R A C T

Surfactants and Digital Alloys for Strain Relief in III-Nitride Distributed Bragg Reflectors and Related Heterostructures via Metal Organic Vapor Phase Epitaxy

Lee Ellen Rodak

III-Nitride based semiconductors have emerged as one of the promising materials for electronic and opto-electronic devices, including but not limited to, solid state emitters, photodetectors, and transistors. Despite commercial success, several issues ranging from material growth to device fabrication remain unresolved and continue to hinder the efficiency of these devices. One such issue includes strain management in III-Nitride heterostructures. The binary alloys in the (Al,In,Ga)N family are characterized by a large lattice and thermal mismatch which leads to defect formation and cracking within heterostructures. These defects are detrimental to device fabrication and operation. This work investigates growth based techniques to manage strain in III-Nitride heterostructures and thereby reduce defect formation.

In particular, this work focuses on the development surfactant assisted growth and digital alloys as strain relieving techniques to minimize cracking in Aluminum Gallium Nitride (Al\textsubscript{x}Ga\textsubscript{1-x}N) alloys and related heterostructures via Metal Organic Vapor Phase Epitaxy. Indium has been investigated as a surfactant in the growth of AlN/GaN Distributed Bragg Reflectors (DBRs) and has been shown to reduce the cracking by a factor of two. Using variable temperature x-ray diffraction studies, indium has been shown to influence the thermal expansion coefficients of the AlN layers. The digital growth technique has been investigated as a viable method for achieving high quality, crack free Al\textsubscript{x}Ga\textsubscript{1-x}N films. Alloys with an AlN mole fraction ranging from 0.1 to 0.9 have been grown by adjusting the periodicity of these short period superlattice structures. High resolution x-ray diffraction has been used to determine the superlattice period along with the $a$- and $c$-lattice parameter of the structure. High aluminum content digital Al\textsubscript{x}Ga\textsubscript{1-x}N alloys have been employed in DBRs for high reflectivity, >94%, crack-free structures. The characterization of these structures via scanning electron microscopy, atomic force microscopy, and x-ray diffraction is presented along with the results from the integration of the DBR with visible wavelength LEDs.
I would genuinely like to thank the individuals who have assisted me in completing the work presented in this dissertation.

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<tbody>
<tr>
<td>2D</td>
<td>Two Dimensional</td>
</tr>
<tr>
<td>3D</td>
<td>Three Dimensional</td>
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<tr>
<td>AFM</td>
<td>Atomic Force Microscopy</td>
</tr>
<tr>
<td>AlN</td>
<td>Aluminum Nitride</td>
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<tr>
<td>Al(<em>x)Ga(</em>{1-x})N</td>
<td>Aluminum Gallium Nitride</td>
</tr>
<tr>
<td>BOE</td>
<td>Buffered Oxide Etch</td>
</tr>
<tr>
<td>Cp(_2)Mg</td>
<td>Bis-cyclopentadienyl Magnesium</td>
</tr>
<tr>
<td>DBR</td>
<td>Distributed Bragg Reflector</td>
</tr>
<tr>
<td>DOE</td>
<td>Department of Energy</td>
</tr>
<tr>
<td>EDS</td>
<td>Energy Dispersive X-Ray Spectroscopy</td>
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<tr>
<td>EL</td>
<td>Electroluminescence</td>
</tr>
<tr>
<td>ELOG</td>
<td>Epitaxial Lateral Overgrowth</td>
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<tr>
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<td>Full Width Half Maximum</td>
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<tr>
<td>GaAs</td>
<td>Gallium Arsenide</td>
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<td>GaN</td>
<td>Gallium Nitride</td>
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<tr>
<td>GMS</td>
<td>Gas Mixing Cabinet</td>
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<tr>
<td>HEMT</td>
<td>High Electron Mobility Transistor</td>
</tr>
<tr>
<td>HVPE</td>
<td>Hydride Vapor Phase Epitaxy</td>
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<tr>
<td>In(<em>x)Ga(</em>{1-x})N</td>
<td>Indium Gallium Nitride</td>
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<td>IQE</td>
<td>Internal Quantum Efficiency</td>
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<td>Molecular Beam Epitaxy</td>
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MEMs : Micro-electro-mechanical Systems
MFC : Mass Flow Controller
MO : Metal Organic
MOVPE : Metal Organic Vapor Phase Epitaxy
MQW : Multi-Quantum Well
NEMs : Nano-electro-mechanical Systems
NH₃ : Ammonia
PC : Pressure Controller
PECVD : Plasma Enhanced Chemical Vapor Deposition
RCLED : Resonant Cavity Light Emitting Diode
RF : Radio Frequency
RMS : Root Mean Square
SAG : Selective Area Growth
SCCM : Standard Cubic Centimeters per Minute
SEM : Scanning Electron Microscopy
TEC : Thermal Expansion Coefficient
TEGa : Triethylgallium
TMAI : Trimethylaluminum
TMIn : Trimethylindium
TMGa : Trimethylgallium
UV : Ultra Violet
XRD : X-Ray Diffraction
LIST OF SYMBOLS

$a_o$ : Nominal $a$-lattice Parameter
$c$ : Speed of Light in Vacuum
$c_o$ : Nominal $c$-lattice Parameter
$D$ : Diffusion Constant
$d$ : Interplanar Spacing
$E$ : Energy
$E_g$ : Bandgap
$p$ : Elasto-optic Coefficient
$M$ : Biaxial Modulus
$n$ : Refractive Index
$S_{ij}$ : Elastic Coefficients
$s$ : Film Thickness
$v$ : Speed of Light in a Medium with Refractive Index ‘$n$’
$Y$ : Optical Impedance

$\alpha$ : Absorption Coefficient
$\Delta$ : Ellipsometry Phase Component
$\epsilon$ : Permittivity
$\varepsilon$ : Strain
$\lambda$ : Wavelength
$\zeta$ : Stress
$\mu$ : Permeability
$\nu$ : Frequency of Light
$\rho$ : Free Charges
$\Psi$ : Ellipsometry Amplitude Component
CHAPTER 1 – INTRODUCTION

1.1 Statement of the Problem

The III-Nitride material system is attractive for a number of optoelectronic and electronic applications including but not limited to, Light Emitting Diodes (LEDs), Laser Diodes (LDs), solar cells, and High Electron Mobility Transistors (HEMTs). Although it has been studied for the last few decades, several issues ranging from material growth to device fabrication remain unresolved. One such issue includes strain management in III-Nitride heterostructures. The binary alloys in the (Al,In,Ga)N family are characterized by a large difference in the lattice parameters which leads to defect formation and cracking within heterostructures. This degradation in material quality is detrimental to device fabrication. This work investigates growth based techniques to manage strain in III-Nitride heterostructures and thereby reduce the defect formation. Specifically, this work develops strain relieving techniques for the growth of high Aluminum (Al) content Aluminum Gallium Nitride (AlxGa1−xN) alloys and Aluminum Nitride (AlN) layers for use in III-Nitride based Distributed Bragg Reflectors (DBRs). Two different strain relieving techniques have been investigated to fabricate high quality, high reflectivity III-Nitride based Distributed Bragg Reflectors for use in visible wavelength optoelectronic device fabrication, such as Light Emitting Diodes (LEDs) or Resonant Cavity LEDs (RCLEDs). A generic structure of a RCLED structure utilizing a III-Nitride DBR is shown in Figure 1-1. The specific means of strain relief targeted in this work include the following:

- The use of indium as a surfactant during Metal Organic Vapor Phase Epitaxy
- The use of a digital alloy growth technique for high aluminum content Aluminum Gallium Nitride alloys

These techniques have been optimized in order to fabricate III-Nitride Based Distributed Bragg Reflectors (DBRs) with a reflectivity greater than 94%. The DBRs have been integrated with visible wavelength LEDs to improve light extraction.

Other defects, such as threading dislocations also continue to plague the efficiency of III-Nitride based devices by forming non-radiative recombination and scattering centers. 

1
Additionally, the piezoelectric and spontaneous polarization in the III-Nitride materials also plagues the operation of LEDs by causing carrier separation. Therefore, in addition to DBR fabrication, this work has also developed the Epitaxial Lateral Overgrowth (ELOG) process and three-dimensional structures using Selective Area Growth (SAG) techniques. The ELOG process reduces the dislocation density in order to improve the LED efficiency and was achieved using a standard two step process. The SAG of GaN has been used to create faceted structures consisting of semipolar and nonpolar planes. Although it is beyond the scope of this work, the SAG structures allow for the engineering of the polarization effects by fabricating devices on non-polar or semi-polar planes.

1.2 Outline

This work addresses both growth based approaches for minimizing strain in III-Nitride heterostructures. Chapter 2 covers the background related to this work. In particular, the properties of III-Nitride materials and also conventional growth techniques are discussed. As this work focuses on growth via MOVPE, this technique is discussed in detail. A relevant literature review of using indium as a surfactant and also digital alloy techniques is included. Also within Chapter 2 are reviews of Distributed Bragg Reflector (DBR) operation and a growth techniques used within the III-Nitride material system.
Chapter 3 contains a summary of the experimental methods used in this work. In particular, a description of the Aixtron MOVPE tool used for material growth is provided. Diagnostic and maintenance procedures developed during the duration of this work are discussed in detail. In order to analyze and extract critical material properties from thin films such as composition, refractive index, and bandgap, many *ex situ* characterization techniques have been performed. In particular, films have been characterized via XRD, transmission, reflectivity, ellipsometry, and Hall Effect measurements. The system description and measurement technique for each is covered in Chapter 3.

Chapter 4 includes a description of the digital alloy growth technique employed in this work. This growth conditions used and the characterization of Al$_x$Ga$_{1-x}$N alloys with an AlN mole fraction ranging from approximately 0 to 1 is discussed. The characterization of strain in these films with respect to the nominal lattice parameter and also the underlying buffer layer is also covered. Finally in this chapter, the influence of temperature on the growth kinetics is presented.

Chapter 5 describes the DBR growth and characterization using both indium as a surfactant and also digital Al$_x$Ga$_{1-x}$N alloys. The growth technique as well as sample characterization including reflectivity, SEM, AFM, and XRD is included. Furthermore, an analysis of the cracking as observed through optical microscope images is discussed.

The integration of the DBRs with visible wavelength LEDs is covered in Chapter 6. In particular, blue In$_x$Ga$_{1-x}$N/GaN based LEDs have been grown on top of high reflectivity DBRs. The analysis of their performance as compared to conventional LEDs on GaN epilayers will be discussed. Characterization includes current-voltage measurements, electro-luminescence, and XRD.

In addition, LEDs have also been integrated on curved GaN surfaces and will be presented in Chapter 7. Using the SAG techniques, curved GaN features have been grown and used as the substrate for LED fabrication. The process flow as well as the LED characterization is included.
Chapter 8 details the SAG and ELOG processes for GaN. The influence of the V/III ratio, growth temperature, and gas flow on the SAG geometry is presented. Optimal conditions for ELOG have been identified and are discussed.

Chapter 9 includes the fabrication of Aluminum Nitride (AlN) suspended structures using a sacrificial layer. Using the growth of AlN on silicon dioxide layers, suspended structures with a wide range of dimensions have been fabricated. These structures have numerous applications in micro-channel and micro-bridge technology given the piezoelectric properties of AlN. The fabrication technique and characterization are presented.

Finally, Chapter 10 contains the conclusions and future direction for this work.
CHAPTER 2 - BACKGROUND

2.1 III-Nitride Materials

III-Nitride based semiconductors possess several properties making them attractive for use in a number of devices, including but not limited to emitters, detectors, and transistors\(^1\). In particular, the wide, direct bandgap of binary, ternary, and quaternary alloys in the (Al,Ga,In)N family can be engineered from approximately 0.7 eV\(^5\) to 6.2 eV\(^2\) depending on the composition as shown in Figure 2-1. This allows the emission wavelength to range over the entire visible spectrum and into the Ultra Violet (UV) regime. This is the only direct semiconductor family that can cover this range\(^6\) as illustrated in Figure 2-2. Alloys in the III-Nitride material family are most thermodynamically stable in the wurtzite crystal structure\(^2\). As shown in Figure 2-3, this is a hexagonal close packed structure consisting of two bilayers, A and B, with stacking sequence ABABAB.... Accordingly, this crystal structure is characterized by two lattice parameters, \(a\) and \(c\), where the \(a\)-lattice parameter is the edge length of the basal hexagon and the \(c\)-lattice parameter is the height of the hexagonal unit cell. The cubic zincblend structure also exists and is commonly seen when III-Nitrides are epitaxially grown on cubic substrates such as silicon or Gallium Arsenide\(^2\). This family of materials is further characterized by mechanical and thermal stability along with chemical inertness enabling its use in harsh environments\(^1\). The high electron saturation velocity and high break down fields are attractive for High Electron Mobility Transistors (HEMTs)\(^1,2\) while the large piezoelectric properties\(^2\) are well suited for Microelectromechanical (MEMs) and Nanoelectromechanical (NEMs) devices.

Due to the range of wavelengths covered by this material family, considerable efforts have focused on fabricating visible and UV devices with III-Nitride materials and also improving the efficiency of such devices for Solid State Lighting (SSL) applications\(^1\). Popular visible lighting applications include traffic lights, signs, automotive, mobile devices, and general white lighting, while UV LEDs are often of interest for applications such as sterilization and biological detection\(^1\). LEDs offer a potential increase in efficiency when compared to current lighting technologies such as incandescent, fluorescent, and high intensity discharge lamps and are being pursued in the United States as an alternative lighting solution\(^7\). The common approaches for creating white light using LEDs includes color mixing and phosphors. In color mixing, emission
Figure 2-1 – a-lattice parameter versus bandgap for (Al,Ga,In)N hexagonal alloys.

Figure 2-2 – Wavelength of emission or detection of various semiconductor materials. Modified from Ref. 6.
from red, green, and blue emitters are combined to produce white light. Within the United States, the Department of Energy (DOE) has established a timeline for the development of SSL over the next 10-15 years. Many of the problems that hinder the use of LEDs for general lighting applications include device efficiency and cost of the materials. Blue nitride based LEDs are commercially available and as of 2009 exhibited Internal Quantum Efficiencies (IQE) of around 80% however, the target established by DOE is 90%.

Green nitride based LEDs present more of a challenge with typical IQEs around 50% in 2009. As with the blue LEDs, the target for green emitting devices is 90%. This lack of efficiency is due to several reasons. For visible emission, typically Indium Gallium Nitride (In$_x$Ga$_{1-x}$N) Multi-Quantum Wells (MQWs) are employed as the active region in the LED devices as shown in the generic structure in Figure 1-1. Green emission requires larger indium concentrations in the In$_x$Ga$_{1-x}$N wells when compared to blue devices as illustrated in Figure 2-1. Unfortunately, the growth of In$_x$Ga$_{1-x}$N is challenging for a number of reasons. Phase separation occurs due to the difference in lattice constants between InN and GaN, and the large immiscibility gap in this system. This results in inhomogeneous In concentrations in planar films in the typical growth window of 600 °C to 800 °C. Higher growth temperature can improve crystallinity, but results in a reduced In concentration. Furthermore, there is a large internal electric field occurring in nitride based devices grown along the $c$-axis due to the nonsymmetric nature of the wurtzite crystal. 

\[ \text{Figure 2-3} \quad \text{Wurtzite structure of GaN (modified from Ref. 2). Stacking sequence consists of two bilayers A and B.} \]

\[ \text{Figure 2-3} \quad \text{Wurtzite structure of GaN (modified from Ref. 2). Stacking sequence consists of two bilayers A and B.} \]
internal electric fields have detrimental effects on emitter devices as they cause carrier separation, reduced recombination, and a redshift in emission. Additional work has focused on the investigation of In$_x$Ga$_{1-x}$N growth on nonpolar and semipolar substrates which is of interest in order to reduce the internal electric field occurring in nitride based materials grown along the $c$ axis.

In a similar fashion, In$_x$Ga$_{1-x}$N MQWs are also attractive for solar cell applications. As already discussed, bandgap of the In$_x$Ga$_{1-x}$N material system covers the visible wavelengths; however it additionally corresponds closely with the solar radiation as shown in Figure 2-4. This makes the alloy a well suited material for photovoltaic applications. Furthermore, the III-Nitride material system has been shown to exhibit a higher resistance to high-energy radiation damage. Space is one of the primary locations for solar cells however, the harsh environment often leads to the degradation of the optical and transport properties in materials such as Gallium Arsenide and Gallium Indium Phosphide. This change has been attributed to defect states induced in the materials. III-Nitride based materials on the other hand, have been shown to be less sensitive to proton and electron bombardment making it a very attractive alternative material.

![Figure 2-4](Image)

*Figure 2-4 – Band gap energies In$_x$Ga$_{1-x}$N materials compared with the air-mass-1.5 solar irradiation spectrum. From Ref. 10*
As shown in Figure 2-1, Aluminum Gallium Nitride (Al\textsubscript{x}Ga\textsubscript{1-x}N) alloys within the III-Nitride family are employed for UV emitting and detecting devices. Solid state emitters offer efficient and portable alternatives to large, toxic gas lasers and as a result are being heavily pursued for medical, sensor, and technological applications\textsuperscript{11}. Medical applications largely involve sterilization of air and water, but also include various forms of fluorescence endoscopy for cancer diagnosis\textsuperscript{12}. Solid state devices provide a compact, rugged, and portable device specifically for such applications. Optical UV biosensors are being pursued for label free detection of molecules\textsuperscript{13,14} and when compared to other types of detection, are inherently simpler and allow for field testing. Finally, UV sources allow for high density optical storage, smaller resolution photolithography processes, and line-of-sight short range communications\textsuperscript{11,15}. Al\textsubscript{x}Ga\textsubscript{1-x}N alloys are heavily pursued for detector fabrication due to the fact that they enable solar blind detector fabrication\textsuperscript{16}. Most UV photodetectors, such as silicon based devices, are sensitive to visible light as well as UV light. However, for certain applications, it is necessary to reject visible emission and focus only on UV wavelengths. Applications include missile tracking, furnace control, engine monitoring, and flame detection\textsuperscript{16}.

In addition to optoelectronic properties, Al\textsubscript{x}Ga\textsubscript{1-x}N alloys have other material properties well suited for transistor applications. As communication technology expands, radio frequency and microwave power amplifiers continue to attract more attention\textsuperscript{17}. Al\textsubscript{x}Ga\textsubscript{1-x}N based High Electron Mobility Transistors (HEMTs) are one of the competitive devices. GaN based devices typically exceeded other materials in terms of high power, voltage, frequency, and temperature operation\textsuperscript{17}. In particular, the high breakdown voltage of GaN permits this material to operate at voltages much larger than other semiconductor devices such as GaAs based transistors\textsuperscript{17,18}. This facilitates the fabrication of smaller devices and eliminates the need for voltage converters in communication systems\textsuperscript{17}. A general schematic for an Al\textsubscript{x}Ga\textsubscript{1-x}N/GaN HEMT is shown in Figure 2-5. This consists of an Al\textsubscript{x}Ga\textsubscript{1-x}N layer on an insulating GaN layer. Typical Al concentrations are around 30\%\textsuperscript{17,18,19}. Due to the large band offset between Al\textsubscript{x}Ga\textsubscript{1-x}N and GaN, a triangle quantum well is formed at the interface as shown in Figure 2-6. As the electrons diffuse from the large bandgap material to the smaller bandgap material they are trapped in the quantum well forming a two-dimensional electron gas. The confined electrons exhibit a higher mobility due to the fact that there is no scattering with ionized impurity atoms. Saturation velocities reach 3x10\textsuperscript{7} cm/s and mobilities reach around 1200 cm\textsuperscript{2}/Vs\textsuperscript{18}. Very high power densities from
Figure 2-5 – Schematic of $\text{Al}_x\text{Ga}_{1-x}\text{N}/\text{GaN}$ HEMT. From Ref. 17.

Figure 2-6 – Band diagram of Ga-polar $\text{Al}_x\text{Ga}_{1-x}\text{N}/\text{GaN}$ junction. From Ref. 17.
30-40 W/mm have been reported\textsuperscript{20}. Remaining issues largely include thermal management of these devices and also defects generated due to a lack of a native substrate. Furthermore, the operation at high voltages causes electric field induced degradation of the devices which remains an area of research interest\textsuperscript{18}.

Gateless HEMT transistors are being pursued for biosensor applications due to the excellent bio-compatibility of the III-Nitrides\textsuperscript{21}. By removing the metal gate shown in Figure 2-5 and attaching bio-agents, the surface potential is modified so that a measurable change in the drain to source current occurs.

Finally III-Nitride alloys exhibit piezoelectric properties suitable for MEMs and NEMs device fabrication\textsuperscript{22}. MEMs typically refer to the fabrication of devices, such as sensors and actuators, which combine electrical and mechanical components on the micrometer or sub-micrometer size scale. Commercially available silicon and germanium devices include inkjet printer heads, accelerometers, pressure sensors, and flow sensors\textsuperscript{22}. While silicon technology is well developed, there is still considerable interest in the use of III-Nitride layers to bring a new functionality to such devices. In particular, the chemical and thermal stability of the III-Nitride materials enables the use of MEMs devices in harsh environments\textsuperscript{22}. Silicon for example, does not maintain its properties at high temperatures\textsuperscript{22}. Furthermore, the bio-compatibility facilitates the fabrication of bio-sensors or bio-compatible devices such as micro-channels\textsuperscript{21}. Using typical growth techniques as described in the later sections, atomic control over layers can be achieved within the III-Nitrides introducing an additional element of control in the design and fabrication of micro and nano-scale devices. Within the III-Nitride family, AlN exhibits the highest piezoelectric response and is widely pursued for MEMs and Surface Acoustic Wave devices\textsuperscript{2}. Although the piezoelectric response is less than common piezoelectrics, such as Lead Zirconate Titanate (PZT) or Zinc Oxide (ZnO), the high thermal stability of AlN\textsuperscript{2,23} is attractive for specific applications.

All of the III-Nitride devices mentioned, including LEDs, photo-detectors, and HEMTs, consist of multi-layer heterostructures typically grown on foreign substrates as illustrated by the device schematic\textsuperscript{24} in Figure 2-7. Foreign substrates such as sapphire and silicon carbide are typically used due to the limited size and cost of bulk III-Nitride substrates. Bulk samples cannot be grown using conventional techniques, such as Czochralski or Bridgman methods due
to the high temperature and nitrogen pressure required to melt III-Nitride alloys (2497 K and 6 GPa respectively for GaN). Several alternative techniques have been used to produce bulk GaN substrates including Hydride Vapor Phase Epitaxy (HVPE) and high pressure solution processes, however, GaN substrates remain expensive and small in size giving them limited use in device fabrication. Typical crystal growth techniques used for heteroepitaxy along with advantages and disadvantages of each are listed in Table 2-1. This work will utilize growth via Metal Organic Vapor Phase Epitaxy (MOVPE).

2.2 Material Growth via Metal Organic Vapor Phase Epitaxy (MOVPE)

MOVPE is a complex growth technique based on both gas phase and surface chemical reactions. In MOVPE, metalorganic and ammonia precursors are supplied in the gas phase to a heated growth site. Common metalorganic precursors, used as the group III supply during the growth of III-Nitrides include Trimethylgallium (TMGa), Trimethylaluminum (TMAI), and Trimethylindium (TMIn). Ammonia (NH₃) is typically employed as the nitrogen source. The process relies on the decomposition of the metalorganic and ammonia sources in order to obtain the metal (Ga, Al, or In) and N species, respectively. The complete pyrolysis occurs mainly on the surface of the heated substrate, where the metal and nitrogen species

Figure 2-7 – Schematic of typical InGaN MQW LED. Modified from Ref. 24.
undergo a chemical reaction and the nitride film is deposited. Using GaN as an example, the basic reaction between TMGa (Ga(CH$_3$)$_3$) and NH$_3$ is the following:

$$\text{Ga(CH}_3\text{)}_3 + \text{NH}_3 \rightarrow \text{GaN} + 3\text{CH}_4$$

Methane, CH$_4$, is a waste by-product and is exhausted from the system. Figure 2-8 illustrates the growth process for GaN. Initially, the precursors enter the growth chamber and are transported through the reactor by forced convection, or flow due to an imposed pressure gradient. For a horizontal reactor design, a region of decreased gas velocity is formed close to any surface in the growth chamber that is parallel to the gas flow. This region of decreased velocity is referred to as the boundary layer. It is commonly approximated that mass transport through the boundary layer occurs via diffusion. While this is not entirely accurate, it is a simplistic model which yields an accurate description of the process. In the real MOVPE process, the hydrodynamic description is complicated by buoyancy forces and natural convection due to the temperature gradient between the heated growth site and cold walls of the growth chamber. These effects are

<table>
<thead>
<tr>
<th>Growth Technique</th>
<th>Description</th>
<th>Advantages</th>
<th>Disadvantages</th>
</tr>
</thead>
<tbody>
<tr>
<td>Liquid Phase Epitaxy (LPE)</td>
<td>Growth from melt</td>
<td>Simple, high purity, thick layers</td>
<td>Poor thickness uniformity, multilayer structures difficult, limited flexibility in commercial applications</td>
</tr>
<tr>
<td>Hydride Vapor Phase Epitaxy (HVPE)</td>
<td>Reaction of hot metal chlorides and ammonia. Hot HCl is passed over group III elements to form metal chlorides</td>
<td>Fast growth rate, well developed</td>
<td>Al containing alloys difficult, hazardous precursors, difficult to control</td>
</tr>
<tr>
<td>Molecular Beam Epitaxy (MBE)</td>
<td>Elemental sources are evaporated onto heated substrate under UHV</td>
<td>High purity, uniform growth, abrupt interfaces, in situ monitoring</td>
<td>Slow growth rates (Low throughput), expensive operation (time and supplies)</td>
</tr>
<tr>
<td>Metal Organic Vapor Phase Epitaxy (MOVPE)</td>
<td>Gaseous precursors are supplied to heated growth site where pyrolysis occurs</td>
<td>Flexible, abrupt interfaces, high purity, large area growth, selective area growth, in situ monitoring</td>
<td>Most parameters to control during growth, hazardous precursors, expensive reactants</td>
</tr>
</tbody>
</table>
highly dependent on reactor geometry and must be modeled accordingly\(^2^6\). Upon reaching the heated growth site, the precursors undergo thermal decomposition. Although ammonia is very commonly used in MOVPE growth, it is not an ideal source due to its high thermal stability. Approximately 15% of ammonia decomposes at GaN growth temperatures of 950 °C\(^2^6\) requiring an excess of NH\(_3\) to be supplied during growth. Conversely, TMGa is almost completely dissociated at 500 °C\(^2^6\). The species decomposing before reaching the substrate continue to diffuse to the substrate due to the concentration gradients. Other species may not fully dissociate until reaching the heated substrate surface. Assuming the temperature is sufficient, the species diffuse on the substrate surface to an energetically favorable location at which point they are incorporated into the GaN film via a surface chemical reaction. Under ideal conditions, the

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\(^{26}\)"Figure 2-8 – MOVPE Growth Process Diagram."
The growth surface is comprised of atomically flat terraces separated by steps as shown in Figure 2-8. Growth is modeled and has been observed in many crystals to occur by the propagation of these steps\textsuperscript{26}.

In general, growth can be categorized into two cases, kinetically limited and mass transport limited. Over a certain temperature range, growth is independent of temperature and is referred to as the mass transport limited regime\textsuperscript{26}. The limiting factor in this case is gas phase diffusion of the precursors to the growth site. Under these conditions, the growth rate has been empirically shown to vary linearly with group III supply. As mentioned previously, the amount of ammonia supplied during growth is much greater than the amount of TMGa, giving rise to a V/III ratio $>> 1$, where the V/III ratio is the ratio of NH\textsubscript{3} to TMGa supplied. It is therefore assumed that at the growth interface, there are considerably more NH\textsubscript{3} molecules than TMGa molecules. Since GaN is stoichiometric and the same number of Ga and N atoms are incorporated into the solid, the growth rate of the film is therefore proportional to the concentration of the Ga atoms reaching the surface. Accordingly, the growth rate can be approximately by the diffusion of Ga atoms through the boundary layer\textsuperscript{26}:

\[
\frac{ds}{dt} = D \frac{dp_{Ga}}{dx}
\]

Eq. 2-1

In this equation $s$ is the film thickness, $t$ is the time, $D$ is a diffusion constant of Ga through the boundary layer dependent on reactor conditions such as temperature and pressure, $p_{Ga}$ is the concentration of Ga, and $x$ is the distance traveled through the boundary layer. Typically, it is desirable to conduct growth in the mass transport limited regime in order to obtain the highest growth rate efficiency (i.e. ratio of growth rate to the input molar flow). In the kinetically limited case, the growth rate is dependent on the temperature due to the influence of temperature on the pyrolysis of the precursors, diffusion of the adatoms on the surface, and also the desorption of the precursors from the surface\textsuperscript{26}.

**2.3 III-Nitride Growth**

**2.3.1 Gallium Nitride Growth**

During MOVPE growth, GaN does not wet sapphire well and therefore direct growth on sapphire results in three dimension growth\textsuperscript{2} yielding material that is not suitable for device
fabrication. The use of buffer and nucleation layers have been employed in order to achieve high quality films with smooth morphology. In particular, either low temperature AlN buffer layers or low temperature GaN nucleation layers have been used successfully on (0001) c-plane sapphire for subsequent GaN growth\textsuperscript{1,2,27}. In both cases, the purpose of the layer is to provide nucleation centers for the high temperature GaN growth.

The growth process for GaN on sapphire using an AlN buffer layer is shown in Figure 2-9. Low temperature AlN layers, deposited around 600 °C, are mainly amorphous layers. This layer is partially crystallized as the temperature is increased to a GaN growth temperature around 950-1000 °C and forms a columnar structure on which the high temperature GaN nucleates. Initially, the GaN also grows in a columnar structure similar to that of the AlN. However, the GaN crystals well oriented with the substrate (along the c-axis) exhibit a faster growth rate as compared to the mis-oriented crystals and form GaN islands. These islands laterally expand to coalesce and form a smooth GaN layer\textsuperscript{28}.

Growth using a GaN nucleation center is similar to that with an AlN layer. Growth begins with the low temperature GaN deposition resulting in small GaN islands with columnar structure. Next high temperature annealing and growth are performed in which these islands laterally expand and coalesce to also form a smooth layer\textsuperscript{2}.

In either case, a large number of dislocations, or line defects in the crystal, are generated at the interface between sapphire and GaN. As growth proceeds, there is a reduction in dislocation density due to the formation of loops and partial dislocations. After approximately 300 nm, the remaining dislocations are vertically threading in the <0001> direction and continue to propagate throughout the film growth\textsuperscript{2} as shown in the Transmission Electron Microscopy (TEM) image\textsuperscript{29} in Figure 2-10. These dislocations are attributed by some to accommodate the twist and tilt in the coalescing islands\textsuperscript{30} or from faulted regions in the nucleation layer\textsuperscript{28,31}. Despite the generation mechanism, threading dislocations are by far the most predominant defect in GaN with densities ranging from $10^9$ to $10^{11}$ cm$^{-2}$ for MOVPE grown films on sapphire substrates\textsuperscript{1}. After the initial filtering of the dislocations close to the substrate interface, three vertically threading dislocations are observed in high quality GaN layers: edge, screw, and mixed dislocations\textsuperscript{1,30,31}. Of typical defect densities, approximately 1% are screw dislocations with a
Figure 2-9 – GaN growth on sapphire using AlN nucleation layer. From Ref. 2.

Figure 2-10 – TEM image of GaN film grown on sapphire. High densities of dislocations are formed close to the sapphire interface. From Ref. 29.
roughly equal number of edge and mixed dislocations accounting for the rest\textsuperscript{30}. While long lifetime devices have been fabricated on such defective material, the threading dislocations are still limiting nitride based device development. Dislocations act as non-radiative recombination centers for electron-hole pairs\textsuperscript{1} and reduce the efficiency of such devices. Furthermore, dislocations act as Columbic scattering centers limiting the mobility of carriers in the material and as current leakage paths in device\textsuperscript{1}. As a result several techniques have been developed for decreasing the number of dislocation densities in these films such as Epitaxial Lateral Overgrowth (ELOG), Pendo-Epitaxy, and Cantilever Epitaxy\textsuperscript{1}.

### 2.3.2 Epitaxial Lateral Overgrowth

Both One Step ELOG and Two Step ELOG methods used in commonly used in MOVPE growth\textsuperscript{1,30}. In both techniques, the process begins with a 1-2 μm GaN epilayer grown on a foreign substrate such as sapphire. Next a thin, approximately 100 – 200 nm, silicon dioxide or silicon nitride layer is deposited. Standard UV photolithography and wet etching techniques are used to create a striped pattern in the oxide along the $<1\overline{1}00>$ GaN direction as shown in Figure 2-11a. This direction is used due to experimental results indicating this direction facilitates faster lateral overgrowth compared stripes oriented along the $<1\overline{1}20>$ GaN direction\textsuperscript{34}. Various dimensions of the striped pattern are used, but examples include a 3 μm window width and 7 μm mask width\textsuperscript{30}. In One Step ELOG, the sample is returned to the MOVPE chamber after patterning for re-growth under conditions promoting fast lateral growth. Such growth conditions include high temperature, low pressure, or high V/III ratio\textsuperscript{1}. Growth begins in the window region on the exposed GaN and then extends laterally over the mask region as shown in Figure 2-11b. Eventually the two wing regions coalesce over the mask and result in a smooth film. The oxide mask blocks any dislocations propagating towards the surface resulting in virtually dislocation free material above the mask. However, at the coalescence boundary defects are introduced. The material grown above the window region reflects the quality of the epilayer below and the dislocations continue to propagate to the surface. Before coalescence the growth exhibits a rectangular cross section bounded by the (0001) plane and vertical \{11\overline{2}0\} sidewalls as illustrated in Figure 2-11b. The reduction in dislocation density averaged over the entire surface is highly dependent on the ratio of mask width to window width. The larger this ratio, the wider the area of low defect material and thus a lower overall
dislocation density is achieved\textsuperscript{30}. With a reported GaN defect density of mid $10^8$ cm\textsuperscript{-2}, it has been observed that using one step ELOG reduces the dislocation density to approximately mid $10^6 - 10^7$ cm\textsuperscript{-2} on average\textsuperscript{1} as determined by TEM and Atomic Force Microscopy (AFM). Often times, upon coalescence a void is formed along $<1\overline{1}00>$ direction but does not impact the quality of subsequently grown material\textsuperscript{1}.

In Two Step ELOG\textsuperscript{1,30,33}, often called FACELO (Facet Controlled Epitaxial Lateral Overgrowth), the process starts with the same silicon dioxide striped pattern on a GaN epilayer as shown in Figure 2-11a. In the first step, growth conditions are identified to promote vertical growth instead of lateral growth. This is commonly accomplished in MOVPE growth by lowering the growth temperature. The objective is to form well defined structures with triangular cross section bounded by the \{1122\} planes as shown in Figure 2-12a. In the second step, the growth conditions are adjusted to promote lateral overgrowth as illustrated in Figure 2-12b. This can include increasing the growth temperature and/or V/III ratio, or by the

\textit{Figure 2-11} – (a) ELOG mask pattern and (b) schematic of One Step Elog Growth.
introduction magnesium in the vapor phase. It has been shown that magnesium facilitates the fast lateral growth by suppressing growth along (0001) plane. When the dislocations encounter the sidewalls of the structure in the first step, they have been observed to bend 90° to run parallel with the basal plane and are thus prevented from reaching the surface of the ELOG layer as shown in Figure 2-12b. All dislocations, edge, screw, or mixed, have been demonstrated to bend. Similarly as described in the One Step process, the dislocations underneath the silicon dioxide mask are also prevented from reaching the surface. At the coalescence boundary, the dislocations that have bent 90° either are annihilated, bend down toward the substrate or bend up toward the surface. It has been observed that the dislocations mainly appear in the center of the mask area and very few appear in the center of the window. The total dislocation density has been reduced with this technique from the mid $10^8$ cm$^{-2}$ to $10^6$ cm$^{-2}$. The bending of the dislocations is often attributed to the stress the dislocation encounters.

Figure 2-12 – Illustration of Two Step ELOG. a) Step 1 involves the formation of structures with triangular cross section. b) In Step 2, lateral growth is induced to achieve coalescence.
as it threads toward a surface that is not normal to the direction of propagation. When the dislocation is sufficiently close to the surface, the dislocation bends to adopt a new direction which minimizes the stress on the free surface\textsuperscript{30}.

Due to the fact that in the Two Step ELOG process, the formation of the structures with a triangular cross section is critical, the effects of the various growth parameters on the morphology of the Selective Area Growth (SAG) of GaN on a striped dielectric pattern in rotational environments is well reported in the literature. The majority of these studies investigated the effect of growth conditions for ELOG. Before coalescence, ELOG growths typically result in three dimensional structures bounded by some combination of the (0001), \{11\bar{2}2\}, and \{11\bar{2}0\} facets and exhibit square, triangular, or trapezoidal cross sections. Growth parameters investigated include temperature, pressure, V/III ratio, and the pattern fill factor. Low growth temperatures result in \{1\bar{1}01\} sidewalls and poor surface morphology due to the decreased migration of gallium species. Increased growth temperature results in \{1\bar{1}22\} sidewalls and improved morphology on the (0001) surface. Even higher growth temperatures lead to the formation of \{1\bar{1}20\} sidewalls\textsuperscript{34}. Sidewall formation can be attributed to the number of dangling bonds on each surface. The \{1\bar{1}20\} plane has fewer dangling bonds when compared to the \{1\bar{1}22\} plane and therefore is energetically favored under high temperatures\textsuperscript{34,35}. The local V/III ratio increases as the ELOG growth laterally extends over the mask and the concentration of gallium species diffusing from the mask to the growth location decreases which also aids in the transformation from \{1\bar{1}22\} facets to \{1\bar{1}20\} facets\textsuperscript{36}. Furthermore, it has been demonstrated that flow modulation of ammonia (NH\textsubscript{3}) can be used to control the morphology\textsuperscript{37}. As the NH\textsubscript{3} interruption time increased, the lateral growth rate increased and is credited to the enhanced diffusion of the gallium species during the NH\textsubscript{3} interruption.

The stress relaxation in ELOG templates has been previously reported. ELOG techniques have been used in HVPE GaN to eliminate cracking indicating a significant change in the strain profiles of ELOG growth\textsuperscript{38}. Furthermore, modeling and experimental analysis has been reported on the evolution of strain in ELOG templates\textsuperscript{39,40} and indicate there is a relaxation of misfit strain which arises in GaN grown on sapphire\textsuperscript{40}. 

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2.3.3 Aluminum Nitride and Aluminum Gallium Nitride Growth

Compared to GaN, the growth of Al containing III-Nitride alloys via MOVPE is challenging due to the high reactivity of Al containing precursors, low mobility of Al species, and finally the large lattice and thermal mismatch between epilayers. The high reactivity of Al containing precursors is minimized by separating the precursors in the growth chamber until before the growth site. In the MOVPE system used in this work, this is accomplished by the quartz plating separating the MO and the ammonia line in the growth chamber as shown in Figure 3-4. Furthermore, Al containing alloys are grown at a relatively low growth pressures when compared to GaN to minimize parasitic reactions. The low mobility of Al species is generally accommodated by using high growth temperatures.

As shown in Table 2-2, AlN and GaN are characterized by approximately 2.4% lattice mismatch. AlN grown coherently on GaN is under tensile strain and often leads to the formation of misfit dislocations and even cracking. A hexagonal crack network is very commonly observed in Al$_x$Ga$_{1-x}$N alloys with Al concentrations greater than 20% when grown via MOVPE or MBE on GaN layers. When grown on silicon substrates, the thermal mismatch becomes the predominant problem leading to cracking of the material during cool down. As material cracking is detrimental to device fabrication, several techniques have been reported to mitigate these issues.

Table 2-2 – Lattice parameters and thermal expansion coefficients of III-Nitride Alloys

<table>
<thead>
<tr>
<th>Material</th>
<th>Lattice Parameters</th>
<th>Thermal Expansion Coefficient</th>
<th>Lattice Parameter at Typical Growth Temperatures (1000 °C)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>a (Å)</td>
<td>c (Å)</td>
<td>a (K$^{-1}$)</td>
</tr>
<tr>
<td>AlN</td>
<td>3.11</td>
<td>4.98</td>
<td>4.2x10$^{-6}$</td>
</tr>
<tr>
<td>GaN</td>
<td>3.189</td>
<td>5.185</td>
<td>5.59x10$^{-6}$</td>
</tr>
<tr>
<td>InN</td>
<td>3.54</td>
<td>5.76</td>
<td>5.6x10$^{-6}$</td>
</tr>
<tr>
<td>Sapphire</td>
<td>4.758</td>
<td>13.0003</td>
<td>7.5x10$^{-6}$</td>
</tr>
<tr>
<td>Rotated Sapphire</td>
<td>2.379</td>
<td>13.0003</td>
<td>7.5x10$^{-6}$</td>
</tr>
</tbody>
</table>
accommodate the strain and minimize or eliminate the cracking. Two of the most commonly reported techniques include the use of AlN interlayers and Al<sub>x</sub>Ga<sub>1-x</sub>N graded buffers. The use of surfactants and digital structures as pursued in this work, are less widely studied in III-Nitride alloys grown via MOVPE.

### 2.3.4 Aluminum Nitride Interlayers

AlN interlayers refer to the use of thin AlN layers, typically grown at low temperatures, inserted before the strained structure as illustrated in Figure 2-13. Typical interlayer thicknesses range from approximately 10 nm to 30 nm. For thick Al<sub>x</sub>Ga<sub>1-x</sub>N alloys, the AlN interlayer has been proven an effective means of eliminating the crack formation. The commonly accepted explanation is that the low growth temperature results in incoherent AlN growth. This effectively de-couples all subsequent layers from that underneath by creating a new seed, or template, for growth. On the contrary, other observations have indicated that micro-cracks are formed in the AlN interlayer during growth and cause compression in subsequent Al<sub>x</sub>Ga<sub>1-x</sub>N layers to prevent cracking. The Al<sub>x</sub>Ga<sub>1-x</sub>N overgrows the cracks which are buried.

![Figure 2-13](image)

*Figure 2-13 – Schematic of Al<sub>x</sub>Ga<sub>1-x</sub>N layers grown on sapphire (a) with and (b) without AlN interlayers. Modified from Ref. 48.*
in the interlayer\textsuperscript{49}. As the growth conditions, such as temperature and V/III ratio, greatly influence the cracking in the interlayer, it can be used to adjust the strain in the subsequent $\text{Al}_{x}\text{Ga}_{1-x}\text{N}$ layer\textsuperscript{49}.

### 2.3.5 Aluminum Gallium Nitride Graded Buffer Layers

$\text{Al}_{x}\text{Ga}_{1-x}\text{N}$ graded buffer layers are commonly used to accommodate the thermal mismatch between III-Nitride layers and silicon substrates\textsuperscript{44}. In graded buffer layers, the Al concentration in a single $\text{Al}_{x}\text{Ga}_{1-x}\text{N}$ layer is gradually increased during the growth of the layer. This results in the gradual change in the lattice parameter. The elimination in cracking upon cool down is attributed to the introduction of compressive strain through the graded buffer layer\textsuperscript{44}.

### 2.3.6 Surfactants

Surfactants are impurities that collect and “float” on the surface during growth. They typically are not incorporated in the growth due to low solubility\textsuperscript{26}. By saturating the surface they allow the species of interest to diffuse to a more energetically favorable location before being incorporated into the material\textsuperscript{26} as illustrated in Figure 2-14. Common surfactants used during III-Nitride growth include bismuth\textsuperscript{51}, antimony\textsuperscript{52}, silicon\textsuperscript{53,54}, and indium\textsuperscript{55,56,57,58}. In MBE growth of dilute nitrides (GaAsN), both bismuth and antimony lead to decreased surface roughness. Additionally, bismuth enabled increased nitrogen incorporation. As compared to MBE, surfactant use in MOVPE is somewhat less common due to potential chemical reactions with the precursors\textsuperscript{26}. Common surfactants include those which are introduced into the reactor for conventional growth purposes, such as silicon and indium which are used for $n$-type doping and $\text{In}_{x}\text{Ga}_{1-x}\text{N}$ growth respectively. Silicon is used as a surfactant in both MOVPE\textsuperscript{54} and MBE\textsuperscript{53} growth but has been linked to a transition from 2D growth to 3D growth. This has potential applications for quantum dot fabrication. Likewise, indium has been employed in both MOVPE\textsuperscript{57,58} and MBE\textsuperscript{55,56}. In contrast to silicon, it has promoted 2D growth in III-Nitrides and resulted in improved surface and crystal morphology. In MBE growth, improved photoluminescence and surface morphology was observed by using a small indium flux during growth of GaN\textsuperscript{56}. More recently, indium has been used to promote the vertical growth of nano wires via MBE\textsuperscript{59}. The indium promotes the diffusion of the gallium species but has negligible incorporation into the lattice. In GaN growth via MOVPE, indium has decreased surface roughness and led to a relaxation in the film\textsuperscript{57,59} as shown in Figure 2-15. In this figure, a
A dramatic decrease in surface roughness can be observed after the initial introduction of indium into the growth. In Figure 2-15b, the arrow indicates the direction of increasing indium flow. Up to certain point, the GaN $a$- and $c$-lattice parameter approach that of bulk material indicating relaxation in the film. For large indium flows, the deviation from the line is explained by the formation of In$_x$Ga$_{1-x}$N alloys. Up to that point, it is assumed that the indium is not incorporated into the lattice positions, but instead incorporated at defect sites. This serves to reduce the energy of the system, relieve strain, and also to pin dislocations to improve the crystal quality$^{57,59}$. In AlN growth, indium has also been used a surfactant to improve surface morphology$^{60}$ as shown in Figure 2-16a. In thin AlN layers for use in multi-quantum wells, the use of indium changed the surface growth kinetics such that a wavy surface resulted. This wavy surface allows for increased storage of excess energy due to the lattice mismatch and prevented crack formation. Furthermore, the decreased FWHM of the rocking curve shown in Figure 2-16b implies improved crystal quality.

Figure 2-14 – Illustration of surfactant effects of indium during AlN growth. Indium floats on the surface to allow the Al species to diffuse to the energetically favorable location where it is incorporated into the lattice.
Other studies have simply reported a decrease in the surface roughness, but not necessarily a change in surface kinetics. Although the reasons for the strain relaxing effects of indium are not well agreed upon in the literature, the effects have been reported by several research groups.

### 2.3.7 Digital Alloys

Digital alloy growth, often called short period superlattices (SPSL), consists of layers of binary or ternary alloys with layer thicknesses of a few monolayers as illustrated in Figure 2-17. It has been a very effective means of growing ternary and quaternary alloys in a number of material systems including the arsenides, phosphides, antimonides, and nitrides. For example, within the III-Nitride system, the digital growth Aluminum Gallium Nitride (Al\(_x\)Ga\(_{1-x}\)N) consists of the repetition of m monolayers of AlN followed by n monolayers of GaN (represented as (AlN)\(_m\) (GaN)\(_n\)). From the growth perspective, digital techniques have presented many advantages over conventional random alloy growth. Digital growth provides an effective means of accurately controlling alloy composition with the number of monolayers in each layer and allowing for very abrupt changes in the concentration profile. Furthermore, it is also often employed to alleviate material insolubility challenges. Effective digital
(GaInAs)$_m$(GaNAs)$_n$ alloys have been used to overcome the nitrogen incorporation challenge in the growth of dilute nitrides$^{69}$ while digital techniques have also been used in AlInGaN alloys to increase indium incorporation$^{68}$. Short period superlattices have also been used for strain engineering in the III-Nitride system to reduce tensile strain$^{70,71}$. As expected, along with the composition, the electronic properties of the material can also be finely tuned using this technique$^{64}$. For the most part, digital techniques are more commonly studied in MBE than MOVPE. In MBE, the technique is very attractive due to the fact that the composition is dependent on shutter timing not incident flux. However, it has been a proven to be a viable

*Figure 2-16 – (a) AFM Image of AlN/GaN MQW(right) and Al(In)N/GaN MQW(left). (b) XRD rocking curve around 0$^{th}$ order satellite peak for AlN/GaN MQW structures with and without indium. From Ref. 60.*
Initial reports of (AlN)$_m$ (GaN)$_n$ digital alloys via MOVPE proved it was an effective means of engineering the Al composition and electronic properties of the films$^{66}$. The most recent work using MOVPE growth is employing a Modulated Precursor Epitaxy Growth (MPEG)$^{41,42}$. In this technique the group III and V precursors are supplied to the growth chamber in separate pulses. High quality films have been achieved, however, for alloys with large Ga content, surface roughening and inhomogeneous composition were observed. This inhomogeneity was eliminated by growing an AlN MPEG monolayer followed by a conventional Al$_x$Ga$_{1-x}$N random alloy monolayer$^{41,42}$.

It should be recognized that the strain formation and distribution in digital alloys is anticipated to be considerably different than in its random Al$_x$Ga$_{1-x}$N counterpart. The biaxial strain in random alloys often leads to a composition pulling effect and thereby a non-uniform composition distribution within a single layer$^{72}$. Sufficient in-plane strain, as observed in high Al content random alloys on GaN epilayers, results in cracking$^3$. However, in short period
superlattices, the strain is distributed between the AlN and GaN layers yielding a different strain profile. As strain management is critical in device design and operation, it is important to understand the strain formation in these digital structures over the entire compositional range in order to fully utilize them in device fabrication.

2.4 Distributed Bragg Reflectors

As previously mentioned, this work targets nitride based DBR fabrication. Quarter wavelength DBRs are long period superlattice structures comprised of alternating layers of materials with differing refractive indices as shown in Figure 2-18. DBRs are designed to be wavelength specific mirrors by making each layer in the superlattice a quarter wavelength thick. In other words, the thickness of each layer, \( s \), is given by

\[
S = \frac{\lambda_o}{4n}
\]

Eq. 2-2

In which \( \lambda_o \) is the target wavelength for reflection and \( n \) is the refractive index of the material at the target wavelength. At each interface light is reflected according to Fresnel’s equation. The equations for light perpendicular to the plane of incidence and parallel to the plane of incidence are shown in the following equations:

\[
r_\perp = \frac{n_1 \cos(\theta) - n_2 \cos(\theta)}{n_1 \cos(\theta) + n_2 \cos(\theta)}
\]

Eq. 2-3

\[
r_\parallel = \frac{n_2 \cos(\theta) - n_1 \cos(\theta)}{n_2 \cos(\theta) + n_1 \cos(\theta)}
\]

Eq. 2-4

For light normal to the interfaces in the DBR, these equations can be simplified to the following:

\[
r = r_\perp = r_\parallel = \frac{n_1 - n_2}{n_1 + n_2}
\]

Eq. 2-5

Light reflecting from a material of lower refractive index \( (n_2 > n_1) \) will undergo a 180° phase change as indicated by the negative value of the equation. The quarter wavelength thickness of each layer allows the reflections of that wavelength to constructively interfere resulting in high reflectivity.
In the III-Nitride system, the DBRs are critical to the fabrication of LEDs. In particular, DBRs can be used to enhance light extraction by reflecting the light that is transmitted from the backside of the device or in Resonant Cavity LEDs (RCLEDs). RCLEDs have enhanced efficiency, spectral purity, directionality and bandwidth compared to regular planar LEDs. In the III-Nitride system, Al$_x$Ga$_{1-x}$N and GaN are pursued as materials due to the large difference in refractive index. Compared with dielectric based DBRs, III-Nitride structures will often consist of 20 to more periods in order to obtain high reflectivity as shown in Figure 2-19.

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*Figure 2-18 – Illustration of DBR. Layer 1 has refractive index $n_1$ and layer 2 has refractive index $n_2$. Image from Ref. 6.*
Due to the large lattice and thermal mismatch between AlN and GaN, a well defined network of cracks has been reported in both AlN/GaN DBRs grown via Metal Organic Vapor Phase Epitaxy (MOVPE)\textsuperscript{76} and Molecular Beam Epitaxy (MBE)\textsuperscript{77} on sapphire substrates. Several different techniques have been investigated in order to reduce or eliminate cracking, such as using Al\textsubscript{x}Ga\textsubscript{1-x}N\textsuperscript{75} or Al\textsubscript{x}In\textsubscript{1-x}N\textsuperscript{80,81,82} in place of AlN to reduce lattice mismatch, inserting superlattice layers either before\textsuperscript{78} or within the DBR structure\textsuperscript{76}, and also the use of asymmetric layers\textsuperscript{77}. These techniques have been successful in eliminating or reducing the cracking, but not without impacting the fabrication and reflectivity of the DBR. The use of Al\textsubscript{x}Ga\textsubscript{1-x}N or Al\textsubscript{x}In\textsubscript{1-x}N instead of AlN decreases the contrast in refractive index which then requires a larger number of periods to achieve equal reflectivity and also reduces the bandwidth. Figure 2-20 shows the change in band width and also increased required number of periods required when using Al\textsubscript{0.35}Ga\textsubscript{0.65}N instead of AlN. 35 pairs of Al\textsubscript{0.35}Ga\textsubscript{0.65}N /GaN layers\textsuperscript{75} are required in order to

![Figure 2-19 – Reflectivity of Al\textsubscript{x}Ga\textsubscript{1-x}N /GaN DBR with increasing number of periods. From Ref. 75.](image)
Figure 2-20 – (a) Reflectivity of 35 pair $\text{Al}_{0.34}\text{Ga}_{0.66}\text{N}/\text{GaN}$ DBR (From Ref. 75) and (b) reflectivity of 20.5 Period $\text{AlN}/\text{GaN}$ DBR (From Ref. 77).
achieved similar reflectivity as that of a 20.5 pair AlN/GaN DBR. Furthermore, as shown in Figure 2-20, the bandwidth decreased from approximately 50 nm for the AlN/GaN DBR to approximately 20 nm for the Al\textsubscript{x}Ga\textsubscript{1-x}/N/GaN DBR. Al\textsubscript{x}In\textsubscript{1-x}/N /GaN structures have also been investigated as a viable alternative to AlN/GaN structures for DBR fabrication. The most widely noted characteristic of Al\textsubscript{x}In\textsubscript{1-x}/N is the very small lattice mismatch to GaN at 17-18% In concentrations as shown in Figure 2-21. The actual lattice matching concentration remains under investigation as there have been reports of deviations from Vegard’s law, but the mismatch is approximately +/- 0.5% for In concentrations ranging from 14 to 22%. These conditions have been used to alleviate strain issues, and when combined with the ~7% refractive index contrast to GaN as shown in Figure 2-22, Al\textsubscript{x}In\textsubscript{1-x}/N is an attractive material for use in Distributed Bragg Reflectors (DBRs). Crack-free, 20 period DBRs with approximately 90% reflectivity and 40 period DBRs with greater than 99% reflectivity have been fabricated with Al\textsubscript{x}In\textsubscript{1-x}/N/GaN grown via Metal Organic Vapor Phase Epitaxy as shown in Figure 2-23. It can be seen that the bandwidth of the reflectivity is between 30-35 nm in both structures. This is still considerable smaller than that of AlN/GaN due to the decrease in refractive index contrast as shown in Figure 2-22. It is also known that these alloys, in particular In\textsubscript{x}Ga\textsubscript{1-x}/N and Al\textsubscript{x}In\textsubscript{1-x}/N, are characterized by phase separation due to a large immiscibility gap giving rise to growth challenges. The large difference in the growth temperature between InN (~600 °C) and AlN (~1100 °C) makes it difficult to obtain high quality material with large In concentrations. Atomic ordering has been observed in both Al\textsubscript{x}In\textsubscript{1-x}/N and In\textsubscript{x}Ga\textsubscript{1-x}/N alloys, but has not been widely studied in Al\textsubscript{x}In\textsubscript{1-x}/N and Al\textsubscript{y}In\textsubscript{x}Ga\textsubscript{1-x-y}/N alloys. The presence of these micro-structures significantly affects the macroscopic properties of the material and therefore requires further investigation.

As mentioned previously, short period superlattices modify the strain profile within epi layers. As a result, superlattice insertion layers have also been used as an effective means of the relieving the strain in both Al\textsubscript{0.35}Ga\textsubscript{0.65}/GaN and AlN/GaN DBR structures. In both cases, the superlattices have been shown to relieve strain and decrease the cracking. Figure 2-24 contains the SEM images, optical microscope images, and reflectivity measurements for Al\textsubscript{0.35}Ga\textsubscript{0.65}/GaN DBRs grown by inserting a superlattice before the DBR and also embedded within the DBR. In this experiment a 40 period Al\textsubscript{y}Ga\textsubscript{x}/N/GaN superlattice was used as the strain reducing layer. The maximum Al concentration in the Al\textsubscript{y}Ga\textsubscript{x}/N film used in the
Figure 2-21 – Lattice constant versus bandgap for (Al,Ga,In)N materials. From Ref. 79.

Figure 2-22 – Refractive index contrast of AlInN alloys as a function of the indium concentration. From Ref. 79.
Figure 2-23 – Reflectivity spectra of (a) 20 and (b) 40 period Al$_x$In$_{1-x}$N/GaN DBR. From Ref. 79,82.
superlattice was less than 10%. There was not a complete elimination of the cracking observed, but a considerable reduction. However, this group reported interference in the reflectivity measurements as a result of the embedded superlattice layers. Other reports using superlattice insertion layers have completely suppressed cracking in 20 pair AlN/GaN DBR structures\textsuperscript{76}. In particular, 5.5 periods of AlN/GaN superlattices have a total thickness equal to a half wavelength were inserted after every five periods of the DBR as shown in Figure 2-25. This technique was used to obtain DBRs having $>90\%$ reflectivity centered around 400-440 nm. No interference in the reflectivity spectra was reported in this work.

![Figure 2-24](image-url) – Cross sectional SEM images, optical microscope images, and reflectivity measurements for 40 period $\text{Al}_{0.35}\text{Ga}_{0.65}\text{N}$/GaN DBRs. From Ref. 78.
Figure 2-25 – (a) Schematic and (b) reflectivity response of 20 pair AlN/GaN DBR with and without 5.5 periods of AlN/GaN superlattice insertion layers. From ref. 76.
CHAPTER 3 – EXPERIMENTAL METHODS

3.1 Introduction

This work has investigated the development and characterization of III-Nitride films and related devices via MOVPE. In this chapter, the equipment used for material growth and characterization is described. XRD, transmission, reflectivity, ellipsometry and Hall measurements were employed to extract critical material properties such as lattice parameters, band gap, refractive index, and carrier concentrations. Each technique is described in this chapter.

3.2 MOVPE System

All growths in this work have been conducted using an Aixtron 200/4 RF-S MOVPE system shown in Figure 3-1. This particular system contains a horizontal reactor with 2” wafer growth capabilities. The horizontal design of the reactor is used to achieve laminar flow allowing for abrupt changes in growth interfaces. The flow chart shown in Figure 3-2 illustrates the various components of the MOVPE system. The main growth takes place in the reactor; however, as illustrated in Figure 3-2, there are a number of different elements which support the process.

The system is controlled remotely with a software program called CACE. All instructions and recipes for the MOVPE are initiated from the software which interfaces with the different components of the system through the electronics rack. The Gas Mixing System (GMS) is a network designed to mix and deliver gasses to the reactor and is shown in Figure 3-3. As timing and composition of gas determines epilayer quality, the gas mixing system is a critical component of the MOVPE system. The primary elements of the GMS include pneumatically controlled valves, pressure controllers, mass flow controllers, group III and V sources, and dopant sources. The vacuum level for this system ranges from 0 to 1000 mbar and is sustained with a rotary vane pump. The glove box is attached to the reactor so that it opens into nitrogen atmosphere to ensure a clean environment for loading and unloading samples as well and preventing oxygen contamination of the growth chamber. The reactor is the stainless steel chamber in which the growth process occurs. Being a horizontal reactor, gas enters at one end of the reactor, flows horizontally through the chamber, and is exhausted through the opposite end as
Figure 3-1 – Aixtron 200/4 RF-S MOVPE System.

Figure 3-2 – MOVPE System Components
illustrated in Figure 3-4. The wafer is located on a silicon carbide coated graphite disc, known as the susceptor, which is rotated during growth to ensure uniform heating and gas distribution over the wafer. The metalorganic and ammonia gas supply are separated by quartz plate in order to avoid parasitic reaction until just before the growth site. The susceptor is heated to the growth temperature (600 °C – 1100 °C) via induction heating. The coil below the susceptor is excited with a radio frequency (RF) current. The alternating current in the coil creates an alternating magnetic field which induces eddy currents in the sucesptor. The resistance of the susceptor leads to $I^2R$ heating. The system is cooled with a closed loop water chiller which circulates distilled water at 16-19 °C to the Reactor, Glove Box, and RF Generator. After passing through the reactor, the process gas is directed to the AIX-TOX scrubber unit. A 30% solution of sulfuric acid and DI water is showered over exhausted gas to decompose the ammonia and excess metal organics.
3.3 Temperature Calibration

Thin film growth is heavily dependent on the substrate temperature during growth\(^{26}\). As a result, special attention must be paid to the calibration of the temperature to adjust for any drift in the equipment. The MOVPE is equipped with a light pipe for temperature readings. The light pipe measures the radiation from the heated susceptor and converts it to a temperature based on sensitivity parameters set in the Luxtron Temperature Controller. In order to properly set the sensitivity factors for the light pipe, a secondary means of measuring the susceptor temperature must be used. In this work, an Omega OS-3000 AS infrared (IR) pyrometer has been used. This particular pyrometer has a detector sensitive in the 2.1 μm – 2.3 μm spectral range. The quartz ceiling in the reactor was removed to open a clear path from the top viewport of the reactor to the susceptor. A clean \(p\)-type silicon (100) was loaded on the susceptor as the reference surface since the emissivity of silicon is well known and is almost constant over the temperature range of interest\(^{86}\), 600°C to 1000°C. The gas flows into the reactor were set to similar conditions used for GaN growth. The surface temperature was of the silicon wafer was measured with the pyrometer and compared to the setpoint. Figure 3-5 shows the discrepancy between the setpoint and the measured temperature before calibration. A second measurement was performed with a fiber optic pyrometer to repeat the measurement with the quartz ceiling in place. The fiber optic pyrometer collects the radiation over a smaller area and therefore can be used to measure the temperature with the ceiling in the reactor as long as the pyrometer is properly aligned over the hole in the ceiling. As shown in the graph, the temperature measured with the ceiling was
approximately the same as that measured without the ceiling. Therefore, all subsequent pyrometer tests were performed without the ceiling using the IR pyrometer gun. Following the instructions outlined in the Appendix A, the sensitivity factors were adjusted until the setpoint was the same as the value measured with the pyrometer as shown in Figure 3-5. At the time of this writing, there were two susceptors that could be used with the MOVPE system and both were tested after the temperature calibration was performed. As shown in this graph, both have similar temperature characteristics. A pyrometer test, as outlined in Appendix B, is done periodically to verify the integrity of the system and also after any maintenance on the RF coil.

![Graph showing measured substrate temperature and setpoint before and after calibration. Eurotherm reading corresponds to the setpoint entered in the CACE software.](image)

*Figure 3-5 – Measured substrate temperature and setpoint before and after calibration. Eurotherm reading corresponds to the setpoint entered in the CACE software.*
3.4 Growth Rate

Knowing the exact growth rate of the materials is critical to both device fabrication and also system maintenance. Essentially, all growth parameters influence the growth rate. In this section, the influence of the temperature, source supply, and reactor pressure will be discussed. In the MOVPE system, the EpiRAS is an in situ monitoring system allows for real time analysis of growth rate and surface quality. This system utilizes normal incidence reflectometry at a single wavelength. Light is directed approximately normal to the surface of the sample through the reactor window and the reflection is measured with a photodetector. The wavelength is chosen such that it is not absorbed by the material of interest. In the case of a GaN epilayer grown on a sapphire substrate, as illustrated in Figure 3-6, the reflection from the surface undergoes a 180° phase shift since the refractive index of GaN, $n = 2.35$, is greater than that of the ambient gas conditions of the reactor. In contrast, the reflections from the interface between the GaN layer and sapphire substrate do not have an associated phase shift since the refractive index of sapphire, $n = 1.78$, is less than that of GaN. Reflections within the sapphire are negligible in this case since the backside of the sapphire wafer is rough and scatters the light. The reflections from the surface of the GaN layer and also from the interface between the GaN and sapphire undergo constructive or destructive interference depending on thickness of the GaN layer. In order for the reflections 1 and 2 shown in Figure 3-6, to undergo constructive interference, the rays traveling within the GaN layer must travel an odd number of

![Figure 3-6 – Illustration of ray paths in GaN film.](image-url)
half wavelengths (i.e. $2s = \frac{2m-1}{2} \lambda$ where $m$ is an integer, $2s$ is the distance traveled by the ray, and $\lambda$ is the wavelength within the material). Figure 3-7 shows the oscillations during growth of GaN as measured with the EpiRAS. The growth rate can be derived from the oscillation period as follows:

$$\frac{ds}{dt} = \frac{\lambda_o}{2nT}$$

Eq. 3-1

where $s$ is the layer thickness, $T$ is the oscillation period, $n$ is the refractive index of the material, and $\lambda_o$ is the incident wavelength. Furthermore, the amplitude of the oscillations seen in Figure 3-7 give insight into the surface quality of the film. A significant decrease in the amplitude indicates roughening of the surface. Whereas a slight decrease may only indicate non-uniformity over the spot sight of the light. The reflectivity in this figure was measured with 650 nm incident light and includes the desorption and AlN nucleation steps as well as the GaN main layer. The periodicity is approximately 282 seconds indicating a growth rate of approximately 29 nm/min or 1750 nm/hr.

*Figure 3-7 – EpiRas oscillations during GaN growth on sapphire.*
As already mentioned, growth can occur in two regions: kinetically limited or transport limited. To achieve the maximum growth efficiency, it is ideal to operate in the mass transport limited regime in which there is little dependence of the growth rate on the temperature. Therefore, the growth rate has been investigated as a function of the temperature to ensure we are growing in the proper window. Using the periodicity of the EpiRAS oscillations to determine the growth rate, the growth temperature was varied by approximately 100 °C during GaN growth. The results are shown in Figure 3-8. At low temperatures, the growth is kinetically limited and the growth rate increases with increasing temperature. Over the mid-temperature region, the growth rate is almost independent of growth temperature. Finally, at high growth temperatures there is a subtle decrease in the growth rate due to desorption of species from the surface. For this work, temperatures in the mass transport limited regime were targeted for GaN growth. It is necessary to periodically verify the growth rate, especially after servicing the reactor.

Within the mass transport limited regime, the growth rate is expected to vary linearly

![Graph showing growth rate vs. temperature](image)

*Figure 3-8 – Growth rate of GaN at different temperatures for various TMGa flow rates.*
with the group-III source since the growth rate defined in Eq. 2-1 depends on the concentration gradient of the group-III species from the inlet to the growth surface. However, this is only true when all the group-III elements at the surface are being incorporated into the growth. Figure 3-9 summarizes the GaN growth rate as a function of the Ga supply. As expected, we observe a linear dependence of the growth rate on the TMGa flow. Therefore, a TMGa flow rate was chosen that results in good surface morphology as well as electrical properties. In this figure, there is an observable shift in the growth rate as measured across two years. This is due to changes in the reactor over time and also the use of new quartz pieces in the chamber.

The relationship between the group-V precursor and the growth rate is the opposite as that for the group-III precursor. Figure 3-10 shows the dependence of the GaN growth rate on the ammonia source and push flow rates. As mentioned previously, the pyrolysis of ammonia is inefficient; therefore the ammonia supply is always supersaturated. Under this condition, increasing the supply does not increase the growth rate as with the group-III supply. However, it

---

**Figure 3-9** – Growth rate of GaN under various TMGa flow rates. Ammonia source was fixed at 1500 sccm and temperature was 990°C.
Figure 3-10 – Growth rate of GaN at different (a) ammonia source flows and (b) ammonia push flows. TMGa flow rate was 22.5 sccm and the temperature was at 990 °C.
increase the parasitic reactions because the metal organic source and this results in a decrease of the growth rate.

In addition to substrate temperature and source supply, the reactor pressure is also an important process parameter. The MOVPE system used in this work is a low pressure system and typically growths are performed at 200 mbar or lower. The low pressure is not necessarily required for MOVPE growth, however it does reduce the parasitic reactions between the precursors. In general, the lower the reactor pressure, the fewer collisions between the atoms in the gas phase. Figure 3-11 shows how the growth rate increases with decreasing pressure.

3.5 Mass Flow Controller Verification

The mass flow controllers (MFCs) are a critical component of the MOVPE system as they control the delivery of the source materials. In order to design and grow accurate structures

![Figure 3-11](image)

*Figure 3-11 – Relationship between the reactor pressure and GaN growth rate. Ammonia source was 1500 sccm, TMGa source was 22.5 sccm, and reactor temperature was 990°C.*
with abrupt interfaces, it is imperative that the MFCs be operating properly. To verify proper operation, rate of fill tests should be periodically performed. The procedure is outlined in Appendix C, however, in general the reactor is pumped down to the base pressure and then refilled with nitrogen or hydrogen through various MFCs. The rate at which the reactor refills is timed. The fill time between multiple MFCs are compared in order to verify the operation. Figure 3-12 illustrates that the fill time for different gas flows is the same between the ammonia MFC and the Run-Hydride MFC. This indicates that the two MFC are supplying the specified amount of gas.

Performing routine maintenance and verifying the results of the tests described in this section are critical to maintaining the system. In order to achieve growths and devices that are repeatable, it is important to verify that the equipment is working properly and also consistently. Small adjustments must be made over time to correct drift in these measurements. In addition to verifying the equipment operation, the material must also be characterized. Common techniques used in this work to measure material properties include XRD, transmission, reflectivity, and also Hall measurements.

![Figure 3-12 – Reactor fill time for different MFCs.](image)

Figure 3-12 – Reactor fill time for different MFCs.
3.6 X-Ray Diffraction

X-Ray Diffraction (XRD) is a non-destructive characterization technique that provides important information regarding the structural properties of the materials. This includes both the \( a \)- and \( c \)-lattice parameters. These properties allow for the verification of the materials grown, calculation of alloy concentration, and measurement of the strain. In this work, all high resolution XRD measurements were taken with a Bruker D-8 Discover four circle diffractometer equipped with a 4 bounce symmetric Ge (022) monochromator. A Goebel mirror was used to collimate the beam prior to the monochromator. Figure 3-13 shows the principle of X-Ray Diffraction\(^{87}\). X-rays incident on a crystal are diffracted from the individual atoms in the lattice. Constructive interference occurs when distance AB in this figure is equal to \( 2d \), in which \( d \) is the lattice plane spacing. Under this condition, Bragg’s law is satisfied and the relationship between the place spacing (\( d \)) and the angle of incident x-rays (\( \theta \)) is given by\(^{87}\):

\[
n\lambda = 2dsin(\theta)
\]

Eq. 3-2

In which \( n \) is the order of diffraction, \( \lambda \) is the wavelength of the x-ray radiation, \( d \) is the lattice spacing, and \( \theta \) is the angle between the incident x-ray beam and the sample surface. Usually, the

![Figure 3-13 – Illustration of X-Ray Diffraction. Modified from Ref. 87.](image-url)
experimental setup measures 2θ and is therefore represented as the x-axis in 2θ-ω scans. These scans are typically used to determine the out-of-plane lattice parameter and also superlattice periodicity. The presence of satellite peaks in 2θ-ω scans verifies the structural integrity of superlattices as these are only present if there are discrete layers in the structure.

Reciprocal space mapping is another XRD technique which is used to extract information related to the strain and also the in-plane lattice parameter. Reciprocal space mappings are the 2θ-ω scans collected around a reciprocal space point. Figure 3-14 illustrates the relationship between the lattice spacing and the reciprocal lattice space. Above the sample surface is a mapping of the reciprocal space lattice. The scattering vector $S_{hkil}$ is equal to $k_h - k_o$. Diffraction occurs when the scattering vector ends at a reciprocal space point. The magnitude of $k_h$ and $k_o$ is $1/\lambda$ in which $\lambda$ is the wavelength of the x-ray source. The orientation of vector S is changed by modifying the angle with which the incident rays meet the sample and also the position of 2θ.

In this work, GaN and related hetero-structures have been investigated. Standard 2θ-ω scans will be used to extract the lattice parameter and verify the growth conditions for the films. Periodically, samples are measured to verify the operation of the MOCVD but also to evaluate the growth conditions developed. This work has focused on superlattices development and will be discussed in subsequent sections. By evaluating the lattice parameter of both the superlattice and also the buffer layer, the strain state of the material can be determined. In relatively symmetric superlattices, the strain is distributed in both the AlN and GaN layers. This strain profile may be considerably different than what accumulates in a random alloy. It is anticipated that the use of the superlattice will allow for higher content alloys to be achieved before the onset of cracking. Reciprocal space mapping also allows for the coherent or incoherent nature of the film to be determined. In coherent films, the lattice parameter of the top layer is dictated by the lattice underneath. Figure 3-15 illustrates this concept. The peak labeled L refers to the $\text{Al}_x\text{Ga}_{1-x}\text{N}$ layer grown on a GaN substrate labeled S. The vertical alignment of the substrate and the layer in Figure 3-15(a) indicates coherent growth. In this regard, the $\text{Al}_x\text{Ga}_{1-x}\text{N}$ layer has the same in-plane lattice parameter as the underlying GaN substrate. Figure 3-15(b) show a relaxed $\text{Al}_x\text{Ga}_{1-x}\text{N}$ layer which does not have the same lattice parameter as the substrate. The relaxation of films occurs in various ways but commonly include large scale cracking or defect formation.
Figure 3-14 – Schematic showing relationship between real lattice parameters and reciprocal lattice space. Modified from Ref. 87.

Figure 3-15 – Example reciprocal space mapping of a (a) strained and (b) relaxed $\text{Al}_x\text{Ga}_{1-x}\text{N}$ layers on GaN. From Ref. 87.
3.7 Transmission Measurements

For the characterization of thin films and Distributed Bragg Reflectors in particular, transmission and reflectivity measurements are used. Transmission measurements provide an accurate means of determining the film thickness, or if the film thickness is well known, it can be used to find the refractive index. Transmission measurements require a white light source and a spectrometer. In this work, measurements have been taken with a halogen/deuteronium white light source and a BW-Tek CCD spectrometer as shown in Figure 3-16. The setup can also be constructed with a white light source, a monochromator, and a photodetector. Figure 3-17 shows the transmission measurement of a GaN film on a sapphire substrate. This measurement was obtained by measuring the transmission of light through the GaN sample and dividing by the transmission through a bare sapphire wafer. For wavelengths below the bandgap (<364 nm) there is absorption as indicated by the transmission being close to 0. For wavelengths above the bandgap the transmission goes to around 1. The oscillations in the transmission spectra arise from the interference due to the thickness of the sample and correspond to the interference expression:

\[ m\lambda = 2ns \]  

Eq. 3-3

In this equation \( m \) is an integer value for the fringe maxima and half integer for fringe minima, \( \lambda \) is the wavelength, \( n \) is the refractive index, and \( s \) is the thickness of the thin film. The thickness of the film can be extracted by plotting \( 1/\lambda \) for each minima and maxima as a function of \( m \) as shown in Figure 3-18. The slope of this line is equal to \( 1/2ns \). Therefore if the refractive index

![Figure 3-16 – Transmission setup using white light source, CCD spectrometer, and fiber optic cables.](image)
Figure 3-17 – Transmission of GaN film on sapphire.

Figure 3-18 – Plot of interference fringes in transmission of GaN on sapphire sample.
is known, the thickness can be extracted, or if the thickness is well known, the refractive index can be extracted. Assuming a refractive index of 2.44 for GaN, Figure 3-18 indicates the thickness of the film was around 1648 nm.

Transmission can also provide useful information about the bandgap of the thin film. The intensity of light transmitted through the sample depends on the absorption coefficient and is given by:

\[ I_t = I_o e^{-\alpha s} \]  

Eq. 3-4

In this expression, \( I_t \) is the intensity of the transmitted light, \( I_o \) is the incident light intensity, \( \alpha \) is the absorption coefficient, and \( s \) is the sample thickness. The absorption coefficient is related to the bandgap in the following equation:

\[ \alpha(E) \propto K \sqrt{E - E_g} \]  

Eq. 3-5

In this expression, \( E \) is energy, \( E_g \) is the bandgap, and \( K \) is a proportionality constant. From this equation, \( \alpha \) has a square root dependence on \( E \) and therefore \( \alpha^2 \) has a linear dependence on \( E \). The bandgap can be found by plotting \( \alpha^2 \) as extracted from Eq. 3-4 as a function of \( E \). In other words, plot \( \left( \frac{I_t}{I_o} \right)^2 \) as a function of energy. In this plot there is a linear region over which a linear fit can be performed to find the bandgap as shown in Figure 3-19. The value at which this line intersects the x-axis is the bandgap. For a more consistent method, the line can be fit tangent to the point where the slope is a maximum.

### 3.8 Reflectivity Measurements

Reflectivity measurements were used in this work for the characterization of the DBR response. A y-shaped, FRP Series Fiber Reflectance Probe was used for this measurement as shown in Figure 3-20. One end of the fiber optic was connected to a BDS-100 tungsten/deuterium white light lamp, one end to the CCD spectrometer, and the third end was oriented normal to the sample surface. The fiber optic cable has a bundle of 7 optical fibers in a stainless steel ferrule at the sample end. Six fibers connect to the light source to deliver the light, while one fiber feeds back to the spectrometer. The reflectivity of the sample was taken and
Figure 3-19 – Extraction of bandgap from transmission measurements.

Figure 3-20 – Setup for measurement the reflectivity of a sample at normal incidence.
divided by the reflectivity measurement from the aluminum coated pyrex mirror. The aluminum coated pyrex mirror is not a perfect reflector and therefore the measurements must be multiplied by the reflectivity of the mirror which is shown in Figure 3-21. An example of a DBR reflectivity spectrum is shown in Figure 3-22.

### 3.9 Ellipsometry

Ellipsometry is a characterization technique that measures the change in polarization of light that is reflected from a thin film in order to characterize the optical properties of thin films\(^90\). Light can be separated into a p-component in which the fields are parallel to the plane of incidence and an s-component in which the fields are perpendicular to the plane of incidence. Ellipsometry measures how these two components of light change upon reflection from a thin film surface. Two polarization states are described by the following:

\[
\rho = \frac{R_p}{R_s} = \tan(\psi)e^{i\Delta}
\]

This equation relates the ratio of reflections in the p and s directions to determine the amplitude of reflected wave, \(\tan(\psi)\), and the phase of the reflected wave, \(\Delta\). From this information, various models can be developed and used to fit the data. Variable Angle Spectroscopic Ellipsometry (VASE) is used to collect reflection data from samples at multiple angles and multiple wavelengths in order to characterize the optical properties. A flow chart of an ellipsometry technique\(^91\) is shown in Figure 3-23. The sample is first measured and then fit to an optical model. The model is adjusted until a fit between the experimental data and model data is achieved with a low mean square error. In this work, ellipsometry is used to extract the refractive index from III-Nitride based films. Measurements were taken with an M-2000U Variable Angle Spectroscopic Ellipsometer and modeled using WVASE32 software, both from J.A. Woollam Co. Inc. The Cauchy dispersion formula was used for modeling the layers.
Figure 3-21 – Reflectivity of the aluminum coated pyrex mirror used in for reflectivity measurements in this work.

Figure 3-22 – Reflectivity of an III-Nitride based DBR.
3.10 Hall Measurements

Hall Effect Measurements are used to verify the carrier concentration and corresponding mobility in $n$-type and $p$-type materials. Hall measurements are performed by applying a magnetic field perpendicular to the direction of current flow in a bar shaped semiconductor. The Lorentz forces on the carriers due to the applied magnetic field deflect the carriers to one side of the sample. The total force due to the applied electric field and also the magnetic field is given by\(^{92}\):

$$F = q(E + v \times B)$$  \hspace{1cm} Eq. 3-7

In which $q$ is the charge of the carrier, $E$ is the electric field, $v$ is the velocity of the carriers, and $B$ is the magnetic field. This force causes carriers to accumulate along one side of the semiconductor creating an electric field, $E_y$, which is perpendicular to both the current flow and also the magnetic field. This new electric field will balance the Lorentz force created by the magnetic field and the remainder of the carriers will experience no net lateral force as they drift through the material. This induced electric field, $E_y$, is known as the Hall Effect and is directly related to the measurable Hall Voltage and Hall coefficient. From these quantities, the carrier
concentration and mobility can be extracted\textsuperscript{92}. The measured Hall voltage, \( V_H \), is given by the following\textsuperscript{92}.

\[
V_H = E_y w
\]  
Eq. 3-8

Where \( w \) is the width of the semiconductor sample. The Hall coefficient can then be calculated from by:

\[
R_H = \frac{V_H t}{IB}
\]  
Eq. 3-9

In which \( t \) is the thickness of the sample, \( I \) is the current, and \( B \) is the magnetic field. The carrier concentration of holes (\( p \)) or electrons (\( n \)) can then be calculated by the following:

\[
p (n) = +(-) \frac{1}{qR_H}
\]  
Eq. 3-10

Using a resistance measurement \( R \), the resistivity of the sample can be found by knowing the sample cross section (\( A \)) and also the length (\( l \)):

\[
\rho = \frac{RA}{l}
\]  
Eq. 3-11

From the resistivity, the mobility can then be calculated as:

\[
\mu = \frac{|R_H|}{\rho}
\]  
Eq. 3-12

The calculations above correspond to a bar type configuration which is very sensitive to sample shape and contact placement. The Van der Pauw technique is more commonly used in order to accommodate irregular shaped samples. In this technique\textsuperscript{93}, four contacts are placed on the periphery of the sample as shown in Figure 3-24. The resistance can be calculated as \( R_{12,34} = V_{34}/l_{12} \). The notation indicates that the voltage is measured between terminals 3 and 4 while current is driven from terminal 1 to 2. Measuring the different configurations, two resistances can be calculated by averaging four measurements\textsuperscript{93}:

\[
R_A = \frac{(R_{21,34} + R_{12,43} + R_{43,12} + R_{34,21})}{4}
\]  
Eq. 3-13

\[
R_B = \frac{(R_{32,41} + R_{23,14} + R_{14,23} + R_{41,32})}{4}
\]  
Eq. 3-14
The sheet resistance can be calculated by solving the following expression for $R_s$:\(^{93}\)

$$\exp(-\pi R_A/R_s) + \exp(-\pi R_B/R_s) = 1 \quad \text{Eq. 3-15}$$

As before, the Hall voltage must be measured in the presence of a magnetic field. The procedure is to apply a magnetic field and then measure the voltage from two terminals while driving current through the other two. Specifically voltages $V_{24P, 42P, 13P,}$ and $V_{31P}$, with $I_{13}, I_{31}, I_{42},$ and $I_{24},$ respectively must be measured. The subscript P indicates a positive magnetic field. In this measurement, the voltage between terminals 2 and 4 is measured while current is driven between terminals 1 and 3. Likewise the voltages $V_{24N, 42N, 13N,}$ and $V_{31N}$ with $I_{13}, I_{31}, I_{42},$ and $I_{24},$ respectively need to be measured. The subscript N indicates the opposite magnetic field. The differences between the voltages can be calculated as follows:\(^{93}\):

$$V_C = V_{24P} - V_{24N} \quad \text{Eq. 3-16}$$
$$V_D = V_{42P} - V_{42N} \quad \text{Eq. 3-17}$$
$$V_E = V_{13P} - V_{13N} \quad \text{Eq. 3-18}$$
$$V_F = V_{31P} - V_{31N} \quad \text{Eq. 3-19}$$

If the sum of these four voltages is positive, then the semiconductor is $p$-type. Accordingly, if the sum of these voltages is negative, the semiconductor is $n$-type. The overall hall voltages is then $V_H = 8/(V_C + V_D + V_E + V_F)$. The sheet carrier density for holes or electrons is calculated by the following:\(^{93}\):

$$p_s = IB/\left[qV_H\right] \quad \text{Eq. 3-20}$$
\[ n_s = |IB/[qV_H]| \]  
\[ p = p_s/d \]  
\[ n = n_s/d \]

The bulk carrier concentration is calculated by dividing by the thickness of the sample.

The mobility can then be calculated from the following equation using the sheet resistance and sheet carrier concentration:

\[ \mu = 1/qn_sR_S \]

To verify the properties of the films grown at WVU, the Ecopia Hall Effect System was used. Samples were cut into squares and indium contacts were soldered to each corner. Gold wire was used to connect the sample to the mount for testing. Periodically, samples were measured in order to verify the operation of the MOCVD and also to optimize the layer properties for devices. Figure 3-26 shows the relationship between the bulk carrier concentration and the mobility for \( n \)-type GaN films. These films were doped with silicon which is achieved by introducing dilute silane (\( \text{SiH}_4 \)) into the growth. Samples grown using both an AlN and GaN buffer layer are shown in this plot and follow the bell shaped trend explained in literature\(^94\). At carrier concentrations less than \( \sim 5 \times 10^{17} \text{ cm}^{-3} \), the mobility is limited by scattering with the dislocations whose density is represented by the dotted lines. Above this concentration, the mobility is influenced by ionized impurity scattering and decreases with increasing carrier concentration. It has been observed at WVU that when the \( n \)-type GaN layer is grown on a insulating GaN epilayer layer that is at least 3 \( \mu \text{m} \) thick, the mobility increases from approximately 70 cm\(^2\)/Vs to 200 cm\(^2\)/Vs. This corresponds to an approximate decrease in the dislocation density from \( 2 \times 10^{10} \text{ cm}^{-2} \) to \( 8 \times 10^{9} \text{ cm}^{-2} \). Therefore, for active devices a \( \sim 3 \mu \text{m} \) GaN buffer layer was employed. The ratio of the silane supplied to the group III source supplied can be used to control the carrier concentration in the film. Figure 3-26 shows the linear relationship between the flows and the carrier concentration. Excess silicon doping will cause GaN to crack due to an increase in the \( a \)-lattice parameter with increasing silicon incorporation\(^95\). Bulk carrier concentrations around \( 1.5 \times 10^{19} \text{ cm}^{-3} \) have been shown to cause cracking in films grown at WVU.
Figure 3-25 – Relationship between carrier mobility and bulk carrier concentration for n-type GaN.

Figure 3-26 – Bulk carrier concentration of n-type GaN as a function of the ratio of silane dopant to group III source supplied.
CHAPTER 4 – DIGITAL ALLOY GROWTH

4.1 Introduction

Digital alloys consist of short period superlattices in which the periodicity is a few monolayers. In this work, Al\textsubscript{x}Ga\textsubscript{1-x}N alloys were grown using a digital technique. High quality Al\textsubscript{x}Ga\textsubscript{1-x}N alloys, as already mentioned, are plagued by several growth challenges. Anytime multiple species are introduced into growth, the kinetics become considerably more complicated. In this work, a digital growth technique is targeted in order to alleviate many of the growth related challenges. Furthermore, the use of modulation doping of the superlattices\textsuperscript{96,97} can eventually lead to a conductive superlattice layer for integration in the DBR structure, although it is beyond the scope of this work. In this work, digital alloys have been fabricated by depositing thin layers of GaN followed by thin layers of AlN as shown in Figure 4-1a. By accurately controlling the number of monolayers in the superlattice, the AlN mole fraction in the Al\textsubscript{x}Ga\textsubscript{1-x}N alloy can be precisely tuned.

The optimized growth conditions for AlN and GaN layers differ significantly. In particular, AlN is typically grown at considerably higher temperatures and lower reactor pressures when compared to GaN. As previously mentioned, this is to increase the mobility of the Al species which has a larger sticking coefficient than Ga and also to limit the parasitic reactions that occur between TMAI and NH\textsubscript{3}\textsuperscript{42}. Therefore, it was the intent of this work to determine a single set of growth conditions that will yield high quality AlN and GaN very thin films. This will allow for fast switching between the layers in the superlattice. The growth temperature targeted in this work was 1000 °C because this results in reasonable quality AlN and also GaN based on previous results of individual thin film growth. However, other temperatures were also investigated. One of the most important parameters under investigation was the reactor pressure as it largely influences the AlN growth. Therefore, growth conditions have been identified that yield both high quality AlN and GaN film growth so that Al\textsubscript{x}Ga\textsubscript{1-x}N alloys with AlN mole fractions ranging from 0 to 1 have been grown by fixing the GaN thickness and changing the AlN thickness.
4.2 Digital Alloy Growth

Digital alloys were grown in the Aixtron 200/4 RF-S MOVPE horizontal reactor. The Ga, Al, and N precursors used for growth were trimethylgallium (TMGa), trimethylaluminum (TMAI), and NH$_3$, respectively. Single sided polished sapphire substrates with a 0.3 degree miscut toward the $m$-plane were used. Prior to growth the sapphire was cleaned \textit{in situ} at 900 $^\circ$C under hydrogen. This cleaning was followed by a 40 nm thick AlN nucleation layer deposited directly on the sapphire. Next, either a 50 nm thick GaN or 200 nm thick AlN buffer layer was grown. The AlN buffer layer was chosen to be thicker than the GaN buffer layer in order to obtain reasonable intensity in subsequent XRD characterization which will be discussed later. After the buffer layer growth, the AlN/GaN superlattice was grown at 1000 $^\circ$C. 40 $\mu$mol/min of TMAI was supplied for the AlN growth and 80 $\mu$mol/min of TMGa was supplied for the GaN growth. 1.2 slpm of NH$_3$ was supplied to the reactor during growth. In this work, the GaN growth time was fixed at 1 second while the AlN growth time was varied from 2 seconds to 188 seconds$^{98,99}$. All of the samples, except those with an AlN mole fraction of 0.8 and above consist of 300 periods. For practical reasons, samples with an AlN mole fraction greater than 0.8 contain between 100-150 periods. In order to ensure minimal precursor mixing and alloy deposition, a three second NH$_3$ purge was used in between the GaN and AlN growth. Figure 4-1 illustrates the structure under investigation and also the growth scheme used in this work. Several growth pressures were investigated as shown in Figure 4-2. At 200 mbar, which is the optimized GaN growth pressure, there was no AlN deposition using this pulse flow technique as indicated by the absence of the Al$_x$Ga$_{1-x}$N (0002) in the 2$\theta$-\$\omega$ scan. At 50 mbar, which is the AlN growth pressure, the digital alloys were characterized by a high density of cracks and also the (0002) peak was split indicating non-uniform composition. Both pressures of 65 mbar and 100 mbar resulted in very minimal cracking and sharp Al$_x$Ga$_{1-x}$N (0002) peaks. For this work, 65 mbar was chosen due to the slow growth rate of AlN at 100 mbar. This set of growth conditions is a compromise between the GaN and AlN growth conditions for single films.
Figure 4-1 – (a) Schematic of Al$_x$Ga$_{1-x}$N digital alloy on sapphire substrate and (b) pulse flow scheme for growth.
4.3 Digital Alloy Characterization

The III-Nitrides have a wurtzite structure and are therefore characterized by an $a$- and $c$-lattice parameter. In this work the $c$-lattice parameter was accurately determined using the symmetric (0002), (0004), and (0006) reflections. The apparent lattice parameters derived from these individual reflections was plotted as a function of $\cos^2(\theta)/\sin(\theta)$ as shown in Figure 4-3\textsuperscript{100}. Since these points are in a straight line, the largest source of error in the measurement is the eccentricity of the sample. The y-intercept of a linear fit of these points yielded the accurate $c$-lattice parameter. Off-axis peaks must be used to determine the $a$-lattice parameter and in this work, the $(\overline{1}014)$ reflection was used. In the hexagonal crystal structure, the relationship between lattice spacing $d$ for a given plane $(hkl)$, and the $c$- and $a$- lattice parameters is as follows\textsuperscript{87}:

$$\frac{1}{d^2_{hkl}} = \frac{4}{3} \frac{h^2 + k^2 + hk}{a^2} + \frac{l^2}{c^2}$$  \hspace{1cm} \text{Eq. 4-1}$$

Figure 4-2 – 2θ-ω scans of the (0002) peaks for digital AlGaN structures grown at reactor pressures of 200, 100, 65, and 50 mbar.
Using the $c$-lattice parameter as previously determined, it was possible to extract the $a$-lattice parameter from the $(\bar{1}014)$ lattice spacing.

$2\theta$-$\omega$ scans of the (0002) reflections of the superlattices contained satellite peaks arising from the periodicity of the structure. The period, $T$, of the was thereby determined from the spacing between the primary and the $+/−$ 1$^{st}$ order satellite peaks using the following expression$^{87}$:

$$T = \frac{\lambda}{2\sin(\theta_1)} - \frac{\lambda}{2\sin(\theta_2)} \quad \text{Eq. 4-2}$$

In this expression, $\lambda$ is the wavelength of the x-rays and $\theta$ is the position of the satellite and primary peaks. Figure 4-4 shows a representative $2\theta$-$\omega$ scan of the (0002) peak of an Al$_x$Ga$_{1-x}$N digital alloy grown on a GaN buffer layer. In this figure the primary satellite peak, $+/−$ 1$^{st}$ order

![Figure 4-3](image.png)

*Figure 4-3 – Apparent lattice parameter of Al$_x$Ga$_{1-x}$N digital alloys calculated from the (0002), (0004), and (0006) reflections.*
satellite peaks, GaN buffer peak, and AlN nucleation layer peaks are visible. The presence of the first order satellite peaks indicates the presence of the superlattice structure. Most likely due to the large periodicity, weak intensity, or interface quality, the higher order satellite peaks were not observed. This is in contrast to some other digital techniques in which apparent intermixing was observed. Figure 4-5 contains the reciprocal space mappings for two representative samples. Figure 4-5a corresponds to an AlN/GaN superlattice grown on the GaN buffer layer in which the superlattice is coherent with the buffer layer. This is indicated by the vertical alignment of the two peaks. Figure 4-5b contains the mapping of a superlattice with thicker AlN layers also grown on the GaN buffer layer. As evident from the vertical misalignment of the two peaks, this superlattice is not coherent with the underlying buffer layer. The buffer layers used in this work were not fully relaxed because of the large lattice and thermal mismatch between the layers and the sapphire substrate and their thickness. The measured in-plane lattice parameter for the

*Figure 4-4 – 2θ-ω scan of the (0002) peak of a (AlN)_m(GaN)_n alloy grown on GaN buffer.*
Figure 4-5 – Reciprocal space map of (1014) peak for two different (AlN)$_m$(GaN)$_n$ alloys grown on GaN buffer. (a) Shows a pseudo-coherent film and (b) shows a relax film.
GaN buffer layer was slightly less than nominal indicating compressive strain. On the other hand, the in-lattice parameter of the AlN buffer layer was slightly larger than nominal. This may be due to the growth conditions employed since it is well known that the growth kinetics largely influence relaxation in III-Nitride films.

Figure 4-6 contains the period, as extracted from the 2θ-ω scans, of the Al$_x$Ga$_{1-x}$N digital alloy as a function of the AlN growth time for samples grown on a GaN buffer layer and on an AlN buffer layer. As already mentioned, for each sample, the GaN growth time was 1 second. To obtain alloys of different concentrations, the AlN growth time was increased from 2 second up to 188 seconds. It can be seen from Figure 4-6 that this time range corresponds to a superlattice period varying linearly from approximately 10 Å to 95 Å and is independent of the buffer layer. The error in the measurement of the period ranges from approximately 2 x 10^{-3} Å to 2 x 10^{-2} Å and is dominated by the sometimes irregular shape of the satellite peaks. The error bars are small compared with the measurement and therefore are not visible in the figure. A linear fit of the data yields an AlN growth rate of approximately 0.45 Å/s. The y-intercept from the linear fit yields a GaN thickness per period of approximately 9.4 Å. The thicknesses of the GaN layers do not correspond to the expected integer value of a monolayer. This type of discrepancy has been reported before in Al$_x$Ga$_{1-x}$N/AlN short period superlattices by Chandolu et al. In that work, potential explanations for the discrepancies include stacking faults, inversion domain boundaries, strain in the superlattice, composition fluctuations, intermixing, and/or interface roughness. In this work, the influence of stacking faults and inversion domain boundaries are neglected because stacking faults do not influence the 2θ-ω scans and inversion domain boundaries are primary found in films doped with magnesium. The superlattices under investigation contain binary alloys (AlN/GaN) and therefore composition fluctuation is also not a concern. Chandolu et al. concluded for Al$_x$Ga$_{1-x}$N/AlN short period superlattices grown via MBE, interface roughness and screw dislocations are largely responsible for the observed discrepancy as they were able to rule out strain. However, in this work, we cannot rule out biaxial strain. Therefore, it is our best estimate that the discrepancy in measured thickness is due to a combination of strain, interface roughness, and dislocations. Figure 4-7 contains the effective AlN mole fraction for the Al$_x$Ga$_{1-x}$N digital alloys. This is a calculated concentration...
based on the thickness of each period and the assumption that each period contains approximately 9.4 Å of GaN as extracted from Figure 4-6 using the following equation:

$$\text{AlN Mole Fraction} = \frac{\text{Period} - 9.4\text{Å}}{\text{Period}}$$  \hspace{1cm} \text{Eq. 4-3}

The dotted line is the theoretical AlN mole fraction based on the AlN growth rate of 0.45 Å/s. As it can be seen, Al$_x$Ga$_{1-x}$N alloys with concentrations ranging $x = 0.1$ to $x = 0.9$ were successfully grown using this digital technique. In order to validate this method of calculating the Al composition in the alloys, Vegard’s law was also used. The films in this work exhibited...
considerable biaxial strain as shown in Figure 4-8 and Figure 4-9. In the absence of biaxial strain, the lattice parameter for $\mathrm{Al}_x\mathrm{Ga}_{1-x}\mathrm{N}$ films will vary linearly with the AlN mole fraction according to Vegard’s law. This nominally relaxed relationship for $\mathrm{Al}_x\mathrm{Ga}_{1-x}\mathrm{N}$ alloys is shown in Figure 4-8 and Figure 4-9 as the straight line connecting the two star points. However, the digital alloys appear to be under in-plane tensile biaxial strain as they appear to fall on the right side of the nominal line. The dashed lines indicate the change in lattice parameters for an $\mathrm{Al}_x\mathrm{Ga}_{1-x}\mathrm{N}$ film of a specific Al composition, $x$, under biaxial strain. The relationship between the $c$- and $a$- lattice parameters is given by the following:\(^{103}\):

$$\frac{c_s-c_o}{c_o} = -2\frac{S_{13}}{S_{33}}\frac{a_s-a_o}{a_o}$$  \hspace{1cm} Eq. 4-4

In this equation, $c_s$ is the $c$-lattice parameter of the strained film, $c_o$ is the nominally relaxed lattice parameter, $a_s$ is the $a$-lattice parameter of the strained film, $a_o$ is the nominally relaxed $a$-
lattice parameter, and $S_{13}$ and $S_{33}$ are the elastic constants for the respective films. The lattice constants and elastic coefficients used in this work were taken from the literature and are shown in Table 4-1. A linear extrapolation of the elastic constants between AlN and GaN was used for the Al$_x$Ga$_{1-x}$N films. A comparison of the lattice parameter extracted from the superlattice period and also that from the $a$- and $c$-lattice parameters is shown in Figure 4-10. It is evident that the two methods of calculating the AlN mole fraction result in similar values. Finally, Energy Dispersive X-Ray Spectroscopy (EDS) was also used to verify the AlN mole fraction on a few representative samples. Figure 4-11 shows that this third technique is also in good agreement with the AlN mole fraction calculated from the periodicity of the superlattice.

![Figure 4-8](image)

*Figure 4-8 – $a$- and $c$- lattice parameters for digital alloys grown on GaN buffer layers.*
Table 4-1 – Lattice parameters and elastic coefficients for GaN$^{43}$ and AlN$^{2}$

<table>
<thead>
<tr>
<th></th>
<th>$a_{o}$ (Å)</th>
<th>$c_{o}$ (Å)</th>
<th>$S_{13}$ (GPa)</th>
<th>$S_{33}$ (GPa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>GaN</td>
<td>3.189</td>
<td>5.185</td>
<td>106</td>
<td>398</td>
</tr>
<tr>
<td>AlN</td>
<td>3.11</td>
<td>4.98</td>
<td>120</td>
<td>395</td>
</tr>
</tbody>
</table>

Figure 4-9 – $a$- and $c$-lattice parameters for digital alloys grown on AlN buffer layers.
Figure 4-10 – Comparison of AlN mole fraction calculated from the periodicity and from Vegard’s law.

Figure 4-11 – Comparison of AlN mole fraction calculated from the periodicity and EDS measurements. Plot includes digital alloys grown on GaN and AlN buffer layers and also random alloys grown on GaN buffer layers.
In addition to the series of digital alloys, both AlN on AlN and GaN buffer layers were grown, as well as GaN on GaN and AlN buffers at the same growth conditions as the digital alloys. The lattice parameters from these samples were extracted and used as the reference parameters for strain measurements instead of their respective nominal values in order to account for differences in growth methods.

In-plane strain in materials is often measured with respect to the nominal relaxed lattice parameter \(a_o\) or the underlying buffer layer lattice parameter \(a_{\text{buffer}}\). The strain with respect to the relaxed film provides insight into the evolution of the strain in the material. However, measuring the strain with respect to the buffer layer allows the conditions for coherent growth, if possible, to be determined. In this work, we will discuss both measurements.

Figure 4-12 contains the strain taken with reference to the nominally relaxed \(\text{Al}_x\text{Ga}_{1-x}\text{N}\) film and is given by

\[
\varepsilon_a = \frac{a_{\text{measured}} - a_o}{a_o} \quad \text{Eq. 4-5}
\]

in which \(a_o\) is the nominal lattice parameter and the \(a_{\text{measured}}\) is the lattice parameter for the superlattice. The nominally relaxed parameter is calculated using Vegard’s law and the measured AlN and GaN lattice parameters grown on each respective buffer layer. The strain profiles on each buffer layer are similar as shown in Figure 4-11. For the digital alloys grown on the GaN buffer layer, the effective digital alloy accumulates an increasing amount of strain up to an AlN mole fraction of 0.3. After which there is a decrease in the stored energy and the lattice constant moves close to the nominal value. Beyond an AlN mole fraction of 0.5, the sign of the strain changes. This is attributed to the fact that for high AlN mole fractions, the AlN in the superlattice dominates the lattice parameter and the measured superlattice parameter is therefore smaller than the nominal value. A similar trend is observed for the digital alloys grown on AlN buffer layers.

Figure 4-13 shows the in-plane strain measured with respect to the buffer layer given by

\[
\varepsilon_a = \frac{a_{\text{measured}} - a_{\text{buffer}}}{a_{\text{buffer}}} \quad \text{Eq. 4-6}
\]
in which \( a_{\text{buffer}} \) is the in-plane lattice parameter of the buffer layer and the \( a_{\text{measured}} \) is the lattice parameter for the superlattice. There is more error in calculating the in-plane lattice parameter of the buffer layer due to the thickness of this layer when compared to the superlattice. As can be seen, Al\(_x\)Ga\(_{1-x}\)N digital alloys grown on a GaN buffer with an AlN mole fraction from 0 to approximately 0.3 appear coherent (\( a_{\text{measured}} - a_{\text{buffer}} \approx 0 \)). Increasing the AlN mole fraction, by increasing the thickness of the AlN layer, beyond 0.3 results in the apparent relaxation of the digital alloy. It appears that at an AlN mole fraction of approximately 0.8, the AlN in the superlattice dictates the lattice parameter as it has saturated at the strain value for AlN grown on a GaN buffer (no superlattice). For the samples grown on the AlN buffer layer, a similar trend can be observed however, the coherent state is for high Al content alloys. Apparent relaxation occurs for all alloys having an AlN mole fraction less than 0.8. Both set of samples appear to

![Figure 4-12 – In-plane strain in digital alloys with respect to the nominally relax lattice parameter.](image)

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saturate at a strain value of approximately 0.02. This is slightly less than the strain between fully relaxed AlN and GaN (~0.025) indicating that even the binary AlN and GaN layers are not fully relaxed as grown under these conditions. There is no large scale cracking observed in these samples and therefore it can be speculated that the majority of strain relaxation occurs due to the surface roughening, surface undulation, or the generation of dislocations or other defects\textsuperscript{67}, however the resources to accurately determine the defect density are not available. The RMS surface roughness measured by atomic force microscopy (AFM) over 1 $\mu$m\textsuperscript{2} areas for several samples is shown in Figure 4-14. The surface roughness does increase as the AlN layer thickness is increased in the superlattice for growth on both buffer layers. While there is a similar trend observed for growth on both buffer layers, the growth on AlN buffer layers roughens more at high AlN mole fractions as compared to the growth on GaN buffer layers. This roughening may be one possible means of relaxation in the film. It has been reported that the theoretical critical thickness for relaxation of an AlN/GaN superlattice is between 25 Å and 37 Å\textsuperscript{73}. In this work, the onset of relaxation begins at the point in which the superlattice begins

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure4-13.png}
\caption{In-plane strain in digital alloys with respect to the underlying buffer layer.}
\end{figure}
to become incoherent with the buffer layer. For digital alloys grown on GaN buffer layers, this occurs around an AlN thickness of 3.5 Å. This is approximately an order of magnitude thinner than that predicted in the literature, as shown in Figure 4-15. It should be emphasized that the calculations performed by Bykhovski et al. considered a free standing AlN/GaN superlattice of equal layer thickness with no influence from a buffer layer or substrate. Furthermore, growth kinetics play a large role in strain formation in III-Nitride materials and could also contribute to the discrepancy. The relaxation of the digital alloys does follow a trend similar to that predicted by the theoretical work. Figure 4-16 compares the strain in the digital alloys grown in this work with the strain in other short period superlattices grown via MBE. In this figure it is apparent that the relaxation in both studies is taking place at similar thicknesses.

Using this method we have demonstrated a digital alloy technique for the growth of Al<sub>x</sub>Ga<sub>1-x</sub>N alloys with an AlN mole fraction ranging from approximately 0.1 to 0.9. The digital technique consists of the growth of a few monolayers of GaN followed by a few monolayers of AlN. The number of AlN monolayers was used to accurately control the AlN mole fraction in
Figure 4-15 – Comparison of strain in digital alloys and theoretical work.

Figure 4-16 – Comparison of strain in digital alloys and other experimental work on short period superlattices.
the digital alloy. The superlattice structure and relatively abrupt interfaces were verified using XRD measurements. The strain formation in the digital alloy and the influence of the buffer layer was also investigated. There is initially coherent growth for low Al concentration alloys on GaN and for high Al concentration alloys on AlN. Relaxation appears to occur for AlN mole fractions greater than approximately 0.3 for digital alloys grown on GaN and for AlN mole fractions less than approximately 0.8 for digital alloys grown on AlN. There is no large scale cracking observed in these structures and therefore the relaxation is attributed to the formation of misfit dislocations and other defects. The thickness of the AlN in the superlattice at which relaxation begins is approximately an order of magnitude less than theoretical predictions. However, it should be noted that lattice mismatch alone is not solely responsible for the relaxation. The thickness before relaxation largely depends on the growth kinetics and could explain many of the large discrepancies in critical thicknesses reported in the literature.

The bandgap of the digital alloys were measured via transmission measurements as described previously. It was experimentally observed that the bandgap of the alloy was slightly higher than the expected bandgap as calculated from the following expression:

\[ E_{g, \text{AlGaN}} = xE_{g, \text{AlN}} + (1-x)E_{g, \text{GaN}} - xb(1-x) \]  

\text{Eq. 4-7}

In which \( E_g \) is the bandgap of the respective materials, \( x \) is the AlN mole fraction, and \( b \) is the bowing parameter. In this work, the bowing parameter was taken to be 1.2. The bandgap of GaN was 3.4 eV and the bandgap of AlN was 6.2 eV. Figure 4-17 shows the bandgap measured for various alloys as compared to the expected value calculated from the equation above. Due to limitations in the white light source, the high aluminum content alloys could not be accurately measured.

For use in optoelectronic devices, it is also important to determine the refractive index of these digital alloys, as well as how it compares to the random alloy equivalent. Unfortunately, there is considerable discrepancy in the refractive index of III-Nitride films reported in the literature. This may be due to different material quality and strain that arises from variations in the growth parameters. In this work, the refractive index was determined from Variable Angle White Light Ellipsometry. From the superlattice measurements, the thickness of the film is well known, and therefore the ellipsometry models as described previously are easily
Figure 4-18 shows the measured refractive index of digital alloys grown with a 1s GaN growth time along with values from the literature.

The digital alloys discussed thus far corresponds to a set of samples that was grown with a 1s GaN growth time and a variable AlN growth time. Other sets of digital alloys were fabricated by changing the GaN growth time from 1s to 2s and 3s. This increases the GaN layer thickness and changes the AlN mole fraction. Figure 4-19 shows how the periodicity varies for sets of digital alloys grown with 1s GaN, 2s GaN, and 3s GaN growth times. All three sets exhibit a linear dependence on the AlN growth time as expected. The linear fit of each yield an AlN growth rate of around 0.45 Å/s. From the y-intercept of the linear fit we can extract the GaN thickness grown in each period and therefore the GaN growth rate. Figure 4-20 shows the evolution of the growth rate as a function of the GaN thickness. At the interface it appears approximately 9.4 Å is grown in the first 1 second under the conditions used in this work. However, the growth rate is clearly not 9.4 Å/s as can be seen by the periodicity shown in
Figure 4-18 – Refractive index of digital alloys measured from ellipsometry.

Figure 4-19 – Periodicity of digital alloys with varying GaN growth times.
Figure 4-19. After approximately 14 Å of GaN growth, the growth rate appears to approach the nominal value for thick films as shown in Figure 4-20. Thick films correspond to layers greater than 400 nm. This indicates that the growth at the interface is almost twice as fast at the nominal growth rate.

Additionally, the digital alloys were grown at different temperatures to investigate the robustness of the growth technique. Figure 4-21 shows four sets of digital alloy samples all grown with a GaN growth time of 1 s and variable AlN growth time. Samples were grown at 960 °C, 1000 °C, 1032 °C, and 1040 °C and the periodicities have been compared. Figure 4-21 shows there is a considerable change in the periodicity indicating a strong dependence of the superlattice growth rate on growth temperature. This is contrary to the thick film growth rates of AlN and GaN, which have been experimentally verified to be relatively independent of growth temperature over this range.

![Figure 4-20](image.png)

Figure 4-20 – Growth rate of GaN layer in digital alloy as a function of the thickness of the GaN layer. As the thickness increases, the growth rate approaches the nominal value for thick (>500 nm) films.
Figure 4-21 – Periodicity of digital alloys grown with a GaN/AlN growth time a 1s/14s and 1s/8s at various temperatures.
CHAPTER 5 – DISTRIBUTED BRAGG REFLECTORS

6.1 Introduction

Distributed Bragg Reflectors have been investigated in this work for integration in III-Nitride based Light Emitting Diodes (LEDs) based on the structure shown in Figure 1-1 and related devices. In typical III-Nitride based LEDs grown on sapphire substrates, light generated in the active region is emitted from the backside of the device, guided in the sapphire substrate, guided in the GaN slab, or emitted from the top\textsuperscript{104} as shown in Figure 5-1. Only that emission from the top is considered useful for the lighting applications while the rest is loss. In the targeted structure, the DBR is used to reflect light that would be emitted from the backside of the device or trapped in the sapphire substrate, thereby increasing the light extraction efficiency as shown in Figure 5-2. As discussed section 2.4 above, III-Nitride based DBRs are difficult to fabricate due to the large lattice and thermal mismatch between binary materials in this system. In this work, two different techniques have been targeted for the growth of III-Nitride based Distributed Bragg Reflectors (DBRs):

- The use of indium as a surfactant during growth
- The use of a digital alloy growth technique for high aluminum containing alloys

*Figure 5-1 – Oversimplified GaN based LED device showing possible light ray paths. Image modified from Ref. 104.*
6.2 DBR Growth using Indium as a Surfactant

Indium has been used to improve surface morphology and also crystallinity in various III-Nitride alloys\textsuperscript{57,58,60,61}. While it is expected that indium will provide similar strain relaxation in larger period structures, such as the DBR, it must be verified. In these larger period superlattice structures, the strain formation and distribution is considerably different than that in the short period superlattices (MQWs) and random Al\textsubscript{x}Ga\textsubscript{1-x}N alloys referenced previously. In short period AlN/GaN superlattices, the accumulated strain is distributed more equally between the AlN and GaN layers\textsuperscript{73} when compared to larger period superlattices. In random Al\textsubscript{x}Ga\textsubscript{1-x}N alloys, the misfit strain resulting from the lattice mismatch often leads to a composition pulling effect\textsuperscript{72}. The inhomogeneous composition distribution caused by this effect influences the strain formation and relaxation. Since AlN is being used instead of Al\textsubscript{x}Ga\textsubscript{1-x}N in this work, there is no compositional distribution to add an additional strain relief mechanism.

\textit{Figure 5-2} – Targeted structure in this work include the incorporation of III-Nitride based DBR under active region of LED.
In order to determine the influence of indium as a surfactant on the DBR growth, a structure consisting of only six periods was used as shown in the schematic in Figure 5-3. The primary DBR structure under investigation targeted a center reflectivity wavelength of 465 nm due to the fact that DBRs reflecting at this wavelength have practical use in blue LEDs for enhanced light extraction or for Resonant Cavity LED fabrication. Additional structures reflecting at 485 nm and 540 nm, were also investigated. The DBRs were all quarter-wavelength, symmetric structures in which the thickness of each layer is $\lambda/(4*n)$, where $\lambda$ is the center wavelength and $n$ is the refractive index of the material at the center wavelength. For AlN/GaN DBRs targeting reflectivity at 400 nm, no cracking was observed when the structure consisted of five or less periods. As this work primarily targets reflectivity at 465 nm, it can be expected that crack generation might occur in fewer than five periods due to the increased thickness of the layers, but should also occur in a five period structure. In order to isolate the influence of indium on cracking, six period structures were used to exclude, as much as possible, other effects on crack generation. For example, existing defects, such as dislocations and grain boundaries, often serve as the nucleation site for cracks. However, in this work, the resources to accurately define these defect densities are not available. Therefore, the same substrates and growth conditions for the buffer layer were used and it is assumed that the initial dislocation density in each sample is approximately the same.

During the growth of the AlN layer only, trimethylindium was introduced into the reactor as the indium source. Due to the high growth temperature of AlN (~1000 °C), it was expected that there will be minimal incorporation of the indium into the growth as indium containing alloys are grown at a considerably lower temperature (~700 °C). Under these conditions, it is expected that the indium will largely act as a surfactant. The ratio of trimethylindium to trimethylaluminum supplied to the reactor during growth has been varied to investigate the influence of indium. Surface cracking of these structures is visible with the microscope and therefore any changes in the crack density can be clearly observed. Cross sectional SEM images have been used to investigate the layer thickness and also uniformity of the layers. Finally, standard reflectivity measurements were used to verify the reflectivity of the DBRs and any influence of indium on the structures. The reflectivity measurements were performed at normal incidence using a deuterium white light source as described in Chapter 3.
All materials used in this work were grown via MOVPE in the Aixtron 200/4 RF-S horizontal reactor. An approximately 2 μm thick GaN buffer layer grown on c-plane sapphire with a 0.3 degree miscut toward the a-axis was used as the substrate for the DBR growth. Trimethylgallium (TMGa), Trimethylaluminum (TMAI), and ammonia (NH₃) were used as the Ga, Al, and N precursors, respectively. Trimethylindium (TMIn) was introduced into the reactor as the indium source during the growth of the AlN layer only to investigate its influence on cracking. The ratio of the TMIn to TMAI flow rate into the reactor was varied from 0 to 0.6. The growth temperature for the AlN and GaN layers were 1000 °C and 980 °C respectively and the V/III ratio for the AlN and GaN layers were 1100 and 700 respectively. Under these conditions the growth rates for the GaN and AlN layers were approximately 28 nm/min and 10 nm/min respectively.

Quantifying the degree of cracking in thin films is difficult and not commonly reported in the literature. The average distance between cracks has been one metric, but in general, optical microscope images are simply used to visualize the change in crack formation or density. In this work, an image processing technique was employed to quantify the degree of cracking in
the DBR structures and to investigate the influence of indium. Specifically, Nomarski microscope images, as shown in Figure 5-4a, were taken over at least 50% of the sample surface. The MATLAB software program was used to convert the original color image to a binary image with the cracks being white pixels and the sample surface represented by black. The contrast between the crack and the sample surface in the original picture is often not enough to simply convert it to a binary image and have an accurate representation of the surface. Any surface roughness or variation in lighting can result in “noise” in the binary image. Therefore, in this work, the cracks were identified by setting a threshold for the number of white pixels in a row that represented a crack. After converting to a binary image, conventional edge detection using built in MATLAB functions was employed to locate the edges of the cracks as shown in Figure 5-4b. From this image, the average crack length per square millimeter was extracted. The total number of white pixels in this figure was divided by two and converted to millimeters to yield the approximate length of the all the cracks in the image area. This number was normalized by the area of each image. All cracking in the DBR structures exhibited a network similar to that shown in Figure 5-4a. The network consisted of a well defined pattern with intersections

![Figure 5-4](image)

*Figure 5-4 – (a) Original microscope image of AlN/GaN DBR and (b) corresponding binary image.*
occurring at 60° or 120°. For the most part, cracks began or terminated at the intersection with another crack. The MATLAB code for the image processing can be found in Appendix D.

In addition to optical microscope images, cross sectional Scanning Electron Microscopy (SEM) images using a Hitachi S4700 system were used to verify the structure uniformity and layer thickness of the samples. Reflectivity measurements were also and were used as a metric of the DBR’s large scale integrity.

Figure 5-5 contains an SEM image of a representative DBR showing all six periods. The lighter and darker layers correspond to GaN and AlN respectively. This sample was grown with zero μmol/min of TMIn. The only observable difference in the cross sectional SEM images of samples grown with and without indium are the density of cracks, therefore the image shown in Figure 5-5 is representative of all samples used in this work. The layer thicknesses were measured from the SEM images and also from white light ellipsometry measurements using a J.A. Woollam M-2000U Variable Angle Spectroscopic Ellipsometer. The difference between the measured thickness and the targeted thickness was less than 5% and can be attributed to error in the measurement method. Figure 5-6 contains the reflectivity of representative samples exhibiting approximately 60% reflectivity at 465 nm, 485 nm, and 540 nm. Due to limited number of periods, the reflectivity is not characterized by a flat top band width. The simulated value shown was obtained using transmission matrix method\textsuperscript{110} for ideal quarter wavelength DBRs as follows:

\begin{equation}
t = \frac{\lambda}{4} \tag{5-1}
\end{equation}

\begin{equation}
\left[ \frac{B}{C} \right] = \prod_{i=0}^{n} \left[ \frac{\cos(t)}{\sin(t)} \right] \left[ \frac{i \sin(t)}{n \cos(t)} \right] \left[ \frac{1}{n_{\text{subs}}} \right] \tag{5-2}
\end{equation}

\begin{equation}
Y = \frac{C}{B} \tag{5-3}
\end{equation}

\begin{equation}
R = \left( \frac{1-Y}{1+Y} \right) \left( \frac{1-Y}{1+Y} \right) \tag{5-4}
\end{equation}

In these equations, \( t \) is the film thickness, \( n \) is the film refractive index, and \( R \) is the reflectivity. The refractive indices used in the simulations were experimentally measured using white light ellipsometry measurements. The amount of indium introduced during the AlN growth did not
Figure 5-5 – Cross Section SEM image of six period AlN/GaN DBR.

Figure 5-6 – Reflectivity spectra of three different six period AlN/GaN DBR structures.
have any observable effect on the reflectivity measurements and all measured spectra were statistically identical. Similarly, the amount of cracking in the structures did not produce any observable changes in the reflectivity spectra. The simulation results have also been compared with direct numerical calculations using MEEP, a free finite-difference time-domain (FDTD) simulation software package developed at Massachusetts Institute of Technology. The two methods showed similar, if not identical, results and therefore simulations based on the transmission matrix method were preferred due to its efficiency.

Figure 5-7 shows the crack length per square millimeter as a function of the ratio of the TMIn to TMAI flow rate used during the DBR growth. Each point is the average of images taken randomly over approximately 50% of the sample surface. It can been seen in this figure that each samples exhibits a similar standard deviation in cracking across the sample as represented by the error bars. During MOVPE several gradients, including temperature and gas flow, influence growth and often result in a thickness difference across the wafer between 3-25% [111,112]. As a result, the variation in cracking around 10% across the surface of the wafer as shown in Figure 5-7 is considered reasonable and can be attributed these gradients. As can also be observed from this graph, the indium significantly influences the crack formation with an apparent minimum around a ratio of 0.3. Introducing more indium into the system only increases the total crack length. Despite the differences in bi-layer thickness between DBRs reflecting at 460 nm, 485 nm, and 540 nm, the influences of indium during growth is consistent as seen in Figure 5-7. These results indicate that under certain growth conditions, the strain in the DBRs is minimized or at least partially accommodated such that the crack formation is reduced by approximately half. At the high growth temperatures used for AlN and GaN, it was anticipated that the indium incorporation would be minimal due to the high desorption rate of indium. Several samples were measured via Secondary Ion Mass Spectroscopy (SIMS) and show an indium incorporation ranging from $5 \times 10^{17}$ to $10^{18}$ atoms/cm$^3$, even for the highest TMIn flow rate under investigation, as shown in Figure 5-8. This small, but measureable, amount of indium appears to accumulate at the interfaces between the AlN and GaN layers. This work focuses on six period DBRs because it is a complete study in which one growth condition (i.e. indium flow rates) has been investigated. It should be noted however, that using the growth conditions outlined previously, a few samples with a larger number of periods (18) were grown and similar trends in the cracking were observed as illustrated in Figure 5-7. The investigation
of the 18 period structures is not as comprehensive and was used only to verify that the results presented in this manuscript were transferable to structures with a larger number of periods.

Several explanations to the influence of indium in III-Nitride growth has been reported and is generally aimed toward GaN growth. First principle density function calculations have shown that under certain conditions, indium acts as a surfactant. In GaN growth, an indium-adlayer on the film surface allows for enhanced diffusion of nitrogen species and is not expected to be incorporated into the growth. While experimental investigations have reported reduced surface roughness due to surfactant effects, the improvement in the crystal quality has been attributed to the incorporation of indium in lattice defect sites and to solution hardening effects. Reports regarding the growth of AlN using indium have mainly attributed the influence of indium to surfactant effects which promote a change in the growth kinetics by increasing the diffusion length of the aluminum species. The increased diffusivity has resulted in improved surface roughness and spiral step flow growth around screw.

Figure 5-7 – Crack length per square millimeter as a function of the Trimethylindium supplied during growth.
dislocations promoting a wavy surface morphology to reduce elastic strain\(^{60}\). In random Al\(_x\)Ga\(_{1-x}\)N alloys, crack free layers have been achieved using indium during growth\(^{116}\), however the growth kinetics in ternary alloys are different than those in the growth of binary alloys. The sticking coefficients of Ga and Al species are considerably different and therefore require different optimal growth conditions\(^{41}\). As a result, the role of indium in the growth of random Al\(_x\)Ga\(_{1-x}\)N alloys is not necessarily be the same as that in GaN or AlN growth.

It is therefore difficult to speculate whether the reasons governing the effects reported in this paper are due to a change in growth kinetics or due to effects produced by the small incorporation of indium atoms. The use of indium during the growth of AlN/GaN DBRs has enabled a means to partially accommodate the strain in the structure and allow for reduced crack formation. Further investigation into the material properties are required to provide a comprehensive explanation. The increase in cracking with large indium flow rates appears to be in agreement with other reports\(^{57}\) which have suggested that beyond a certain flow rate, indium

\[ \text{Figure 5-8 – SIMs measurement of indium incorporation in AlN/GaN DBR.} \]
no longer acts as a surfactant and is instead incorporated into the lattice during ternary alloy formation.

X-Ray Diffraction measurements were taken in order to further characterize the six period DBR structures. Figure 5-9 shows the (0002) reflection of the six period AlN/GaN DBR structure at room temperature. The lattice parameter for the AlN and the GaN can be derived from this type of plot, and although it is not an accurate measurement of the lattice parameter as discussed previously, it can be used to measure the difference between the AlN and GaN $c$-plane lattice parameter which is shown in Figure 5-10. In this figure, the lattice mismatch between the two layers is plotted as a function of the average cracking in each sample. For all samples under investigation in this work, it is clear that the mismatch between the discrete layers is greater than the nominal value denoted by the solid green line. This indicates biaxial strain in one or both of the DBR layers and is most likely due to in-plane tensile strain in the AlN layer resulting in a reduction in the $c$-lattice parameter for that layer. This effectively increases the difference in the measured $c$-lattice parameters between AlN and GaN. For DBR structures designed to reflect at 460 nm and 485 nm, a similar trend is observed. As the cracking in the samples increases, the

![Figure 5-9 – (0002) reflection of the six period AlN/GaN DBR.](image-url)
difference in the lattice parameters approaches the nominal value. This trend is also evident in the samples reflecting at 540 nm, however, the relaxation appears to have a different slope. Although the mechanism of strain accommodation is not completely understood at this time, it is apparent that under certain conditions, the indium does allow for the strain between the AlN and GaN layer to be accommodated with a minimized formation of cracks.

In order to further investigate this effect, the dependence of the lattice parameters and strain was conducted at different temperatures. In particular, a temperature dependent XRD study was conducted. In this technique, the hot stage was affixed to the Bruker D8-Discover system discussed previously. The temperature of the stage was varied from room temperature to 1023 K and the (0002) scan was evaluated at temperatures in between during both the heating cycle and also during the cooling cycle. Figure 5-11 shows the GaN c-lattice parameter in the AlN/GaN DBR grown using indium as a surfactant as a function of temperature. From this figure it is clear that there was no change induced in the lattice parameter by the thermal cycle as the lattice parameter upon cooling down was the same as the initial value measured. The

![Figure 5-10 – Difference between the AlN and GaN c-lattice parameter derived from the (0002) reflection as a function of the cracking in the films.](image_url)
extracted GaN thermal expansion coefficient (TEC) is consistent with other values reported in the literature as listed in Table 2-2, although there is some discrepancy between different sources. Figure 5-12 shows the apparent AlN c-lattice parameter of the same AlN/GaN DBR as a function of temperature. Unlike that GaN parameter, the thermal cycle did induce a change in the lattice parameter of the AlN layer. Upon cooling down, the AlN returned to a larger lattice parameter than initially measured. A second (0002) scan was performed on this same sample one day later. The lattice parameter measured then was consistent with that measured after cooling the sample down to room temperature, indicating the change in lattice parameter is not a measurement artifact. As a comparison, the same measurement was performed on AlN/GaN DBR samples grown with no indium. Figure 5-13 and Figure 5-14 show the percent mismatch between the AlN and GaN lattice parameters for DBR samples grown with no indium and also that grown with and TMIn/TMAl ratio of 0.3. The sample grown with no indium exhibits no change in the lattice parameter mismatch due to the thermal cycle. In this sample, both the AlN and GaN layers returned to their original lattice parameters. However, the sample grown with a TMIn/TMAl ratio of 0.3 shows a clear change due to the thermal cycle. Upon cooling down, a
Figure 5-12 – Apparent AlN c-lattice parameter in the AlN/GaN DBR as a function of temperature.

Figure 5-13 – Percent mismatch between the AlN and GaN lattice parameters in DBRs grown with no indium.
reduction in the mismatch was observed consistent with the increase in the AlN lattice parameter. No additional cracking was observed after the thermal cycle in either sample. From this, it can be concluded that the minimal incorporation of indium may be changing the material properties.

6.4 DBRs using Strain Relieving Interlayer

From the discussion above, the use of indium enabled a reduction in the cracking of AlN/GaN DBRs by a factor of two. In order to further accommodate the strain in the AlN/GaN DBRs, a GaN interlayer was inserted in the center of the DBR stack. This was done to effectively decouple the top and bottom stack to minimize the strain accumulation. In order to achieve high reflectivity (>90%) the targeted structures were 12 and 18 period DBRs. The interlayer was formed by varying the GaN thickness in the sixth or ninth period of the DBR. In addition to changing the strain profile in the superlattice, the interlayer also changes the reflectivity spectra. Through simulations, it was determined that the interlayer must be an odd multiple of a quarter wavelength thickness in order to have the peak reflectivity at the designed wavelength. For even multiples, there is a sharp dip at the target wavelength as shown in Figure 5-15. Therefore, for the purposes of this work an 11λ/4 interlayer was used. Figure 5-16
contains the cross sectional SEM image showing the fabricated structure. This technique did yield crack-free DBR structures over a 2” wafer on miscut sapphire substrates, however cracking was observed when grown on non-miscut substrates. Figure 5-17 and Figure 5-18 show the measured and simulated reflectivity spectra for the 12 and 18 period structures. It is clear that the 12 period exhibited a similar shape to the simulated response, however the maximum reflectivity was considerably less. This may be due to the variation in the refractive index of the layers or the thicknesses. The 18 period structure however was closer to the target reflectivity of 90%. In order to be integrated into active devices, the surface morphology of the DBRs needs to be epi-ready. Figure 5-19 shows the surface morphology of 600 nm of GaN layer grown on the surface of the 12 period DBR showing step morphology and a smooth surface. While this is suitable for device growth, the reflectivity spectrum has unique shape that may not be ideal. Since the amount of cracking observed in this technique was found to be dependent on the sapphire substrate used a more robust technique was pursued.

Figure 5-15 – Simulated reflectivity of 12 period AlN/GaN DBR with (11λ/4) and (12λ/4) interlayer thicknesses.
Figure 5-16 – Cross sectional SEM image of 12 period AlN/GaN DBR with 11λ/4 GaN interlayer.

Figure 5-17 – Measured reflectivity for 12 period AlN/GaN DBR with 11λ/4 GaN interlayer.
Figure 5-18 – Measured reflectivity for an 18 period AlN/GaN DBR with 11λ/4 GaN interlayer.

Figure 5-19 – AFM image of 600 nm nGaN overgrowth on 12 period AlN/GaN DBR with interlayer. Z-scale is 30 nm.
6.5 DBR Growth using Digital Alloys

In this task, the AlN layers in the DBR as shown in Figure 5-3 were replaced with an optimized Al\(_x\)Ga\(_{1-x}\)N digital alloy having a high Al content. The targeted Al concentration was around 90\% in order to maximize the refractive index contrast between the layers to minimize the number of periods required. As described previously, superlattice structures have been inserted in III-Nitride based DBRs\(^{76,78}\) and used to reduce cracking. In this technique a superlattice is integrated into each period of the DBR, as shown in Figure 5-20, to relieve strain. The growth conditions for the digital alloy are the same as those discussed in Chapter 4. Structures with periods ranging from 6 to 25 have been fabricated in order to achieve high reflectivity DBRs which can be used in LED device fabrication. These structures have been evaluated via SEM, XRD, and AFM to verify the structural integrity.

All the DBR structures were grown in the Aixtron MOVPE system at WVU. The structures were grown on \(c\)-plane sapphire substrates with a \(\sim2\ \mu m\) GaN buffer layer. The growth conditions discussed previously for the 90\% Al content digital alloy was used for the Al\(_x\)Ga\(_{1-x}\)N layer. This corresponds to a 1 s GaN growth time and a 188 s AlN growth time. Two different growth conditions were investigated for the GaN layer. In the first method, the GaN layer was grown at the conditions optimized for GaN on sapphire growth.

![Figure 5-20 – Schematic of DBR containing GaN and digital Al\(_x\)Ga\(_{1-x}\)N layers.](image-url)
These are the same conditions used for the underlying buffer layer. Figure 5-21 shows the cross sectional SEM image of a 12 period DBR grown under these conditions. The interfaces appear sharp, however, the GaN layer thickness does not appear uniform throughout the periods. Therefore, the growth conditions for the GaN layer were modified in order to minimize switching times between the layers. The growth conditions used for the GaN in the digital alloy were the employed for the GaN layer in the DBR. Figure 5-22 contains a cross sectional SEM image of a 25 period DBR grown under the modified conditions. In these structures, the GaN layer was considerably more uniform throughout each layer. Therefore, these growth conditions were used in subsequent characterization and device fabrication.

DBR structures with varying number of periods have been fabricated and the peak reflectivity followed the calculated reflectivity as is shown in Figure 5-23. The following equation was used to calculate the relationship between the reflectivity and the number of periods assuming \( n_{\text{GaN}} = 2.4, n_{\text{AlGaN}} = 2.17, \) and \( n_{\text{Sub}} = 1.78: \)

\[
R_{\text{th}} = 1 - 4 \left( \frac{n_L}{n_H} \right)^{2N_{\text{pair}}} \frac{n_{\text{Sub}}}{n_H^2} \]

Eq. 5-5

In this expression, \( n_L \) is the refractive index of the lower index material, \( n_H \) is the refractive index of the higher index material, and \( n_{\text{Sub}} \) is the refractive index of the substrate. The experimentally measured reflectivity for several representative DBR samples with differing number of periods is shown in Figure 5-24. For these samples, the targeted reflectivity was around 460 nm. It can be seen from this figure that that DBRs with up to 25 periods have been fabricated, which a maximum reflectivity around 95%. In order to fully evaluate the 25 period digital \( \text{Al}_x\text{Ga}_{1-x}\text{N}/\text{GaN} \) DBR, a corresponding 25 period \( \text{AlN}/\text{GaN} \) DBR was also fabricated. Figure 5-25 shows the surfaces of the two DBR samples. The digital \( \text{Al}_x\text{Ga}_{1-x}\text{N}/\text{GaN} \) DBR was characterized by a crack-free surface, while the corresponding \( \text{AlN}/\text{GaN} \) DBR contained the network of cracks typical in such growths. It should also be noted that samples containing digital alloys with fewer than 25 periods were also characterized by a crack-free surface. This verifies that the digital alloy can be used for strain relief and crack suppression in high reflectivity structures. Figure 5-26 shows the reflectivity for the two structures. While the peak reflectivity is the similar, the stop band width of the digital \( \text{Al}_x\text{Ga}_{1-x}\text{N}/\text{GaN} \) DBR is slightly less than the corresponding \( \text{AlN}/\text{GaN} \) DBR due to the reduced refractive index contrast.
Figure 5-21 – Cross sectional SEM image of a 12 period digital Al$_x$Ga$_{1-x}$N/GaN DBR.
Figure 5-22 – Cross sectional SEM image of 25 period digital Al$_x$Ga$_{1-x}$N/GaN DBR in which the growth conditions have been modified for faster switching times.
Figure 5-23 – Reflectivity of Digital Al$_x$Ga$_{1-x}$N/GaN DBR as a function of the number of periods.

Figure 5-24 – Reflectivity of digital Al$_x$Ga$_{1-x}$N/GaN DBRs with total periods ranging from 6 to 25.
Figure 5-25 – Microscope images of the surface of a 25 period digital Al$_x$Ga$_{1-x}$N/GaN DBR and a 25 period AlN/GaN DBR.

Figure 5-26 – Reflectivity of 25 period digital Al$_x$Ga$_{1-x}$N/GaN DBR and 25 period AlN/GaN DBR.
The digital DBRs were further characterized by XRD in order to evaluate the structure. Figure 5-27 and Figure 5-28 contain an on-axis scan and an off-axis scan respectively. From the on-axis scan of the (0002) peak, it is shown that the Al$_x$Ga$_{1-x}$N in the DBR corresponds to the targeted 90% Al concentration, which is determined by the lattice parameter extracted from the peak position. This is in contrast to the AlN peaks in the AlN/GaN DBR which is also shown in Figure 5-27. The off-axis scan maps the (0114) reflection of the digital DBR. In this scan, the GaN and Al$_x$Ga$_{1-x}$N peak are not coherent which is indicated by the vertical mis-alignment of the two peaks. Although the digital DBRs are crack free, it is not a coherent growth. The relaxation may be occurring through some mechanism other than cracking, perhaps defect generation or surface roughening.

In this work, the target was 2” crack free DBRs with consistent reflectivity over the 2” wafer. While samples sizes around ¼ of a 2” wafer exhibited crack-free surfaces, due to non-uniformities in the growth rate across the 2” wafer, cracking was observed at

![Graph showing XRD spectra of AlN/GaN DBR and Digital AlGaN/GaN DBR]

*Figure 5-27 – (0002) reflection of 25 period digital Al$_x$Ga$_{1-x}$N/GaN DBR and 25 period AlN/GaN DBR.*
the edge of 2” DBR growths. In this region the peak reflectivity was also red shifted indicating thicker layers at the edge of the wafer. Figure 5-29 shows the cracking as a function of the radial distance from the center of the wafer. This measurement was performed using images taken over the surface of the wafer. In general, cracking was present 3-6 mm from the edge of the wafer. Figure 5-30 shows the value of the reflectivity over a 2” wafer for a DBR designed to reflect at 452 nm. Close to the edge of the wafer, there is an observable decrease in the reflectivity value as the peak reflectivity shifts away from the targeted value of 452 nm. As mentioned previously, it was experimentally observed that the peak reflectivity shifts to longer wavelengths toward the outer edge of the wafer as shown in Figure 5-31.

One of the last metrics for this growth technique that was evaluated was the transferability. In particular, it was the goal to develop a growth technique that could be transferred easily between different substrates with few modifications required. To verify the versatility of this digital DBR growth technique, a 20 period digital Al\textsubscript{x}Ga\textsubscript{1-x}N/GaN DBR was grown on several different substrates. The cracking, surface roughness, and reflectivity were evaluated in order to determine any differences induced by the substrates. In particular, two different types of non-miscut sapphire and a GaN epilayer on a patterned sapphire substrate (PSS) provided by a collaborator were investigated. The sapphire was from different vendors and had different specifications. 20 period digital Al\textsubscript{x}Ga\textsubscript{1-x}N/GaN DBRs were grown on these substrates with no modifications made to the recipes. All three substrates exhibited minimal cracking around the edge of the wafer only. Figure 5-32 contains the AFM images of the surface morphology both before and after the DBR growth. For the two samples grown using the non-miscut sapphire, the RMS roughness after growth was less than 1 nm. There was a considerable increase in surface roughness for the DBR on the PSS indicating that minor modification may need to be made in the growth to correct for this. However, since the reflectivity and cracking were similar as shown in Figure 5-33, this technique appears to be fairly transferable.
Figure 5-28 – Reciprocal space map of \((-104)\) reflection for 25 period digital \(\text{Al}_{x}\text{Ga}_{1-x}\text{N/GaN}\) DBR.

Figure 5-29 – Cracking in the 2” DBR from the center of the wafer to the edge.
Figure 5-30 – Reflectivity value at 452 nm mapped over a 2” DBR.

Figure 5-31 – Reflectivity of 25 period DBR at different points along the 2” wafer.
Figure 5-32 – Top Row: AFM images of GaN buffer layers used as substrates for DBR growth. Bottom Row: AFM images of 600 nm of n-GaN grown on top of 20 period DBRs.
Figure 5-33 – Top Row: Reflectivity map at 452 nm, Second Row: Reflectivity response at the center of the wafer, Bottom Row: Crack free area from the center for the 2” wafer.
6.4 Oblique Angles of Incidence

All of the DBR reflectivity measurements were taken at normal incidence, however from the targeted structure, shown in Figure 5-2, it is apparent that the light generated in the active region of the LED may not impinge on the DBR at normal incidence. Therefore, it is necessary to understand how the DBR will respond to light incident at an oblique angle.

Waves propagating through a layered structure are partially transmitted and reflected at each boundary. The overall reflection from the structure is given by the superposition of all these waves. The wave transfer matrix is a useful approach to tracking the many reflected and transmitted waves by relating the electric and magnetic fields at the boundaries between layers. The characteristic matrix of the entire structure is found from the multiplication of the individual matrices for each layer making this approach very well suited for superlattice structures. As a result, it is one of the common approaches used for calculating the response of Distributed Bragg Reflectors (DBRs). For a structure with \( q \) dielectric layers, the characteristic matrix is expressed by the following:

\[
\begin{bmatrix}
B \\
C
\end{bmatrix} = \prod_{r=1}^{q} \begin{bmatrix}
\cos(\delta) & -\frac{i}{\eta} \sin(\delta) \\
\frac{i}{\eta} \sin(\delta) & \cos(\delta)
\end{bmatrix} \begin{bmatrix}
1 \\
\eta_{\text{subs}}
\end{bmatrix}
\]

Eq. 5-6

\( B \) and \( C \) are the normalized electric and magnetic field at the initial interface in the structure. \( \delta \) is the phase thickness of each layer and is given by:

\[
\delta = \frac{2\pi n_r d \cos(\theta_r)}{\lambda}
\]

Eq. 5-7

\( n_r \) is the refractive index of the layer, \( \theta_r \) is the angle in the material, \( d \) is the layer thickness, and \( \lambda \) is the wavelength. The characteristic admittance, \( \eta \), depends on the polarization of the light and is expressed for Transverse Magnetic (TM) and Transverse Electric (TE) polarizations as the following:

\[
\eta_{TM} = \frac{n}{\cos(\theta)},
\]

\[
\eta_{TE} = \frac{n_0}{\mu_0} \sqrt{\frac{\mu_0}{\epsilon_0}}
\]

Eq. 5-8
\[ \eta_{TE} = n \sqrt{\frac{\varepsilon_o}{\mu_o}} \cos(\theta). \]  
Eq. 5-9

In these equations, \( \varepsilon_o \) is the permittivity of free space \( (8.8542 \times 10^{-12} \ \text{C}^2/\text{Nm}^2) \) and \( \mu_o \) is the permeability of free space \( (4\pi \times 10^{-7} \ \text{N} \ \text{s}^2/\text{C}^2) \). The optical admittance of the entire structure is expressed by\(^{118} \)

\[ Y = \frac{C}{B}. \]  
Eq. 5-10

Finally, the reflectivity can be calculated from the optical admittance\(^{118} \):

\[ R = \left( \frac{\eta_o - Y}{\eta_o + Y} \right) \left( \frac{\eta_o - Y}{\eta_o + Y} \right). \]  
Eq. 5-11

In this equation, \( \eta_o \) is the admittance of incident medium. Using these relationships, we can determine the influence of the angle of incidence on the reflectivity of an 18 period AlN/GaN DBR structure shown in Figure 5-34. The target wavelength of this structure is 460 nm and each layer is a quarter-wavelength thick. In this calculation, the refractive indices of GaN and AlN are assumed to be 2.44\(^{77} \) and 2.1\(^{119} \) respectively and dispersion has been neglected. Using these values, the AlN layer is 54.76 nm thick and the GaN layer in the DBR is 47.13 nm thick. The angle of incidence was taken within a top GaN layer as the DBR structure is typically under the active region in a GaN-based light emitting diode or laser diode. Using Eqs. 5.6 – 5.11, the reflectivity of the DBR has been calculated at angles of incidence, \( \theta_i \), ranging from 0 to 80 degrees in which 0 degrees corresponds to normal incidence. Figure 5-35 shows the change in the DBR response for several different incident angles, \( \theta_i \). As expected, there is an observable blue-shift in the peak wavelength with increasing angle of incidence. Figure 5-36 contains the reflectivity of the structure at the targeted wavelength of 460 nm for both TE and TM polarizations. The structure maintains relatively high reflectivity for up to approximately 15 degrees, at which point there is a sudden decrease in reflectivity. Above 58 degrees, the angle of incidence is larger than the critical angle and all the light undergoes total internal reflection at the first GaN/AlN boundary. The reflectivity increases sharply to 1 at this angle for both polarizations. From approximately 15 to 58 degrees, there is negligible reflectance from the DBR at the wavelength for which it was designed to operate. Based on the results in Figure 5-35.
and Figure 5-36, to maximize the response of the DBR over a wider range of angles, the DBR could be designed to reflect a wavelength slightly longer than the targeted emission of the device. The Matlab code for generating these plots is found in Appendix E.

Figure 5-34 – Schematic of 18 period AlN/GaN DBR used in the calculation for DBR response at oblique angles of incidence.
Figure 5-35 – Reflectivity of 18 period AlN/GaN DBR at oblique angles of incidence for (a) TE and (b) TM polarizations.
6.5 Influence of Strain on Refractive Index and DBR Reflectivity

Strain induced changes in the refractive index of materials is referred to as the elasto-optic effect or photoelastic effect\textsuperscript{120,121,122}. In III-Nitride materials, strain can arise from the large lattice and thermal mismatch between the layers and the substrates\textsuperscript{2} and also from different growth techniques and/or material quality\textsuperscript{123}. The wide variation in the strain of the materials contributes to the discrepancy in the reported refractive index values of III-Nitride materials\textsuperscript{121,119}. As these materials are of significant interest for opto-electronic device fabrication, it is imperative to accurately determine the refractive index and influence of strain for device design.

Refractive index is the ratio of the speed of light in vacuum to the speed of light in the material\textsuperscript{124}. It is a material property and is given by the following equation\textsuperscript{124}:

\[ n = \frac{c_0}{v} = \sqrt{\frac{\varepsilon \mu}{\varepsilon_0 \mu_0}}. \]  
Eq. 5-12

*Figure 5.36 – Reflectivity at 460 nm for an 18 period AlN/GaN DBR for angles of incidence ranging from 0 to 80 degrees.*
In this equation $n$ is the refractive index, $c_o$ is the speed of light in vacuum ($3 \times 10^8$ m/s), and $v$ is the speed of light in the material of interest. $\epsilon_o$ is the permittivity of free space ($8.8542 \times 10^{-12}$ C$^2$/Nm$^2$) and $\epsilon = \epsilon_o \epsilon_r$ in which $\epsilon_r$ is the relative permittivity of the material. Likewise, $\mu_o$ is the permeability of free space ($4\pi \times 10^{-7}$ N s$^2$/C$^2$) and $\mu = \mu_o \mu_r$ in which $\mu_r$ is the relative permeability of the medium. As this work focuses on nonmagnetic materials, $\mu_r$ is equal to 1 and Eq. 5-12 simplifies to the following:

$$n = \sqrt{\epsilon_r}.$$  \hspace{1cm} \text{Eq. 5-13}

The elasto-optic effect relates the change in the refractive index to the strain in a film by the following:\textsuperscript{121,122,125}

$$\Delta \left( \frac{1}{n^2} \right)_{ij} = \sum_{k,l} p_{ijkl} \epsilon_{kl}. \hspace{1cm} \text{Eq. 5-14}$$

In this equation, $n$ is the refractive index, $p$ is the elasto-optic coefficient tensor, and $\epsilon$ is the strain tensor. In this form, the elasto-optic coefficient is a dimensionless quantity since both strain and refractive index are dimensionless. The strain is calculated by the deviation of the lattice parameter from the nominal value. To calculate in-plane strain, or strain along the $a$-axis in wurtzite crystals, such as the III-Nitrides, the following equation is used:\textsuperscript{121}:

$$\epsilon_{xx} = \frac{a_m - a_o}{a_o} \hspace{1cm} \text{Eq. 5-15}$$

In this expression $a_m$ is the measured $a$-lattice parameter of the film and $a_o$ is the nominal value for a fully relaxed film. In the case of biaxial strain, which is typical in III-Nitrides, it can be assumed that $\epsilon_{xx} = \epsilon_{yy}$\textsuperscript{121,126}. Strain in such films is related to the stress by the following equation\textsuperscript{121}:

$$\sigma_{xx} = M \epsilon_{xx}. \hspace{1cm} \text{Eq. 5-16}$$

In this equation, $\sigma_{xx}$ is the in-plane stress and $M$ is the biaxial modulus given by\textsuperscript{121}

$$M = S_{11} + S_{12} - 2S_{13}^2/S_{33}. \hspace{1cm} \text{Eq. 5-17}$$
$S_{ij}$ are the elastic coefficients of the materials. Using these relationships, the elasto-optic coefficient can be determined in terms of stress instead of strain and in terms of the dielectric constant instead of the refractive index.

Distributed Bragg Reflectors (DBR) are primarily concerned with normal incidence reflectivity, which corresponds to light propagating along the optical axis of the crystal. The relationship between the in-plane strain and the refractive index in this direction is given by the following:

\[
E_{xx} = n(0) - \frac{1}{2}(p_{11} + p_{12})n(0)^3 E_{xx}
\]

Eq. 5-18

In this expression $n(E_{xx})$ is the refractive index as a function of strain, $n(0)$ is the material’s strain-free refractive index, and $p_{11}$ and $p_{12}$ are the components of the elasto-optic tensor that influence the refractive index along this direction. Values for the lattice constants, nominal refractive indices at 460 nm, and elasto-optic coefficients for AlN and GaN are shown in Table 5-1. There are relatively few reports investigating the elasto-optic parameters of III-Nitrides and those shown in Table 5-1 include theoretical and experimental values. The theoretical value for GaN is approximately 4 times less than the experimental value, indicating that the theoretical values may be strongly underestimated. Since the elasto-optic coefficients for AlN and GaN are similar in value, it can be expected that the true value of $p_{11} + p_{12}$ is approximately 4 times the theoretical value shown in Table 5-1. This assumption will be used for the calculations and simulations performed in this work. Using the values in the Table 5-1 and Eq. 5-18, the influence of strain on the refractive index of GaN and AlN is shown in Figure 5-37.

### 6.5.1 Influence of Strain on Reflectivity of Distributed Bragg Reflectors

For a strained AlN/GaN superlattice structure, there are several possible means of relaxation. However, one of the most extreme situations is when there is no relaxation and the superlattice is pseudomorphically grown on a GaN buffer layer. In this case, the AlN layers in the superlattice will be under tensile strain since the lattice parameter of the entire structure is established by underlying buffer layer. In this example, the strain in the AlN layer is approximately 0.025 as calculated from the lattice parameters in Table 5-1 and Eq. 5-15. The
change in the AlN refractive index for this strain value is approximately \( \Delta n = 0.056 \). Performing a simplified simulation of an 18 period AlN/GaN DBR using the transmission matrix\(^77\) method allows us to determine the impact of the refractive index increase on the peak reflectivity. As can been seen in Figure 5-38, the increase in the refractive index of AlN due to tensile strain would result in a decrease in reflectivity around 2%. The simplified simulation shown in Figure 5-38 neglects the dispersion of the refractive index. As the refractive index contrast between AlN and GaN decreases with increasing strain, there is also an observable decrease in the bandwidth of the structure. As mentioned previously, this is an extreme example. It is expected that AlN layers in a DBR reflecting in the visible regime (thickness > 48 nm) would relax through the formation of defects, surface roughness, or cracks\(^67\).

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**Table 5-1 – Constants for AlN and GaN including \( a \) and \( c \)-lattice parameters\(^3\), refractive index\(^119\), and elasto-optic coefficients\(^121,127\).**

<table>
<thead>
<tr>
<th></th>
<th>AlN</th>
<th>GaN</th>
</tr>
</thead>
<tbody>
<tr>
<td>( a_0 ) (Å)</td>
<td>3.11</td>
<td>3.189</td>
</tr>
<tr>
<td>( c_0 ) (Å)</td>
<td>4.98</td>
<td>5.185</td>
</tr>
<tr>
<td>( n ) (460 nm)</td>
<td>2.1</td>
<td>2.44</td>
</tr>
<tr>
<td>( p_{11} + p_{12} )</td>
<td>-0.127 (Theoretical(^{127}))</td>
<td>-0.109 (Theoretical(^{127}))</td>
</tr>
<tr>
<td></td>
<td></td>
<td>-0.460 (Experimental(^{121}))</td>
</tr>
</tbody>
</table>
Figure 5-37 – Refractive index of AlN (left) and GaN (right) at 460 nm as a function of in-plane strain calculated from Eq. 5-18 using the theoretical and experimental values of elasto-optic coefficient shown in Table 5-1. Note: Approximated value of elasto-optic coefficient for AlN was estimated to be $p_{11} + p_{12} = 0.508$. 
6.5.2 Measurement of Refractive Index

Common techniques used to determine the refractive index of materials include transmission\textsuperscript{88}, prism coupling\textsuperscript{121,129}, and spectroscopic ellipsometry\textsuperscript{130}. Transmission measurements allow the refractive index to be determined based on the oscillations arising from optical interference\textsuperscript{88}. It is a useful technique for extracting refractive index and film thickness, however, it is not suitable for very thin films or complex structures. Prism coupling is another technique that is ideal for planar films\textsuperscript{121,129}. In this method a prism is held against the sample surface and used to couple laser light to the sample. The guided modes are identified by changing the angle between the prism and in the incident laser\textsuperscript{121}. As with transmission measurements, this technique is not suitable for very complex structures. The third technique commonly used of the identification of refractive indices and optical properties is the spectroscopic ellipsometry\textsuperscript{130}. This technique is based upon measuring the change in the polarization of light as it interacts with a material. Ellipsometry allows for the modeling of
complex optical systems with multiple layers and materials and may be well suited for DBR modeling.

6.5.3 Measurement of Strain

In order to anticipate the change in refractive index, the strain in the films needs to be determined. One of the more common means to measure strain is the use of X-Ray Diffraction (XRD). On-axis XRD measurements allow the out-of-plane ($c$) lattice parameter to be determined while off-axis scans determine the in-plane ($a$) parameter\textsuperscript{87}. The deviation of the measured parameter from the well documented nominal lattice parameters allow the strain to be calculated from Eq. 5-15. Other techniques for measuring the strain include the use of photoluminescence or reflectivity measurements to determine the exciton energies\textsuperscript{121,131}. Shifts in these energies can be attributed to strain and this method has been used in the III-Nitrides to measure strain in individual layers.
6.1 Introduction

In this chapter, the results of the Light Emitting Diode (LED) and DBR integration are presented. As discussed in the previous chapters, the target for this work is to integrate an LED on a DBR substrate to improve light extraction. The growth as well as the characterization of the devices is discussed.

6.2 Experimental Method

In$_{x}$Ga$_{1-x}$N/GaN based LEDs were grown on a GaN epilayers, 6 period DBR, and 12 period DBR. All DBRs were digital Al$_x$Ga$_{1-x}$N/GaN quarter wavelength DBRs as described in the previous chapter. The total structure is shown in Figure 6-1 and consists of a 30 nm AlN buffer layer, 1.8 μm GaN epilayer, digital Al$_x$Ga$_{1-x}$N/GaN DBR, 1.5 μm Si-doped GaN epilayer, five period In$_x$Ga$_{1-x}$N/GaN multi quantum wells (MQW) targeted for emission at ~ 440 nm, 10 nm

Figure 6-1 – Schematic of the LED structure.
Mg-doped Al\textsubscript{1-x}Ga\textsubscript{x}N electron blocking layer, and a 70 nm Mg-doped GaN epilayer. Prior to LED growth, the reflectivity of 6 period and 12 period digital Al\textsubscript{1-x}Ga\textsubscript{x}N/GaN DBR substrate was measured and is shown in Figure 6-3. The 6 and 12 period DBR exhibited 60\% and 90\% reflectivity, respectively. After the LED growth, the sample was annealed at 800 °C for 5 minutes to activate the Mg dopants. The LED mesas were fabricated using standard UV photolithography patterning and inductively coupled plasma reactive ion etching. The n-type and p-type contacts consisted of Ti/Al/Ti/Au and Ni/Ag/Pt/Ni/Au metal stacks deposited via e-beam evaporation. The p-type contact was a high reflectivity Ag-based contact developed in order to provide a top side mirror\textsuperscript{133}. The final metallization scheme used was a Ni (1 nm)/Ag (150 nm)/Pt (50 nm)/Ni (20 nm)/Au(50 nm) stack followed by an anneal at 430 °C. Characterization of the samples included cross sectional SEM imaging, XRD, I-V, and EL. Image processing techniques were employed to evaluate the emission intensity from the topside of the LEDs

### 6.3 Characterization

Cross sectional SEM images were taken to verify the structure and are shown in Figure 6-3. XRD measurements were taken to measure average indium composition of the MQWs and the period of the superlattice. The MQW growths on the three substrates were similar and had periodicities around 13 nm and an average InN mole fraction around 0.04 as extracted from the (0002) scan as shown in Figure 6-4. Standard I-V measurements were taken and used to determine the turn on voltage as well as the series resistance. Figure 6-7 shows the I-V characteristics of the two LEDs on DBR substrates and the LED on the GaN substrate. The LEDs on DBRs had a turn on voltage of 3.0 V and 2.89 V for the growths on the 6 period and 12 period DBR, respectively. This is slightly higher than the 2.68 V turn on voltage of the LED on the GaN epilayer. The series resistance was determined to be 42.18 Ω, 40.93 Ω, 47.85 Ω for the LED on GaN, the LED on the 6 period DBR, and the LED on the 12 period DBR, respectively. Figure 6-6 shows a optical image of the 12 period DBR with LED under forward bias and also the final fabricated structures on the chip. The structure consists of a 400 μm diameter circular mesa with a centered p-type contact. The n-type contact formed a ring around the mesa.
Figure 6-2 – Reflectivity of the DBR substrates integrated with LED. (a) Six period DBR and (b) 12 period DBR reflectivity spectra.
Figure 6-3 – Cross sectional SEM of LED on (a) 6 period DBR and (b) 12 period DBR.
Figure 6-4 – $\theta$–$\omega$ scan of five $\text{In}_x\text{Ga}_{1-x}\text{N}$ MQWs grown on (a) 6 period DBR substrate and (b) 12 period DBR substrate.

Figure 6-5 – I-V characteristics for LED on DBR samples.
Figure 6-7 shows the room temperature EL spectra of the 3 LEDs. The emission peak wavelength was located around 441 nm and the FWHM of the emission was 17.4 nm and 28.1 nm for LED on the 6 period DBR and the LED on 12 period DBR, respectively. In addition, there are strong vertical modes that emerge in the EL spectra. These modes are formed by vertical distances between layers with differing refractive indices.

Optical images were taken from the top of the LEDs in order to evaluate the extraction intensity. Figure 6-8 shows the normalized light intensity extracted from these images for different samples. In this figure, the sample “LED” refers to a LED on a GaN epilayer. The sample “LED w/Mirror” refers to the LED on a GaN epilayer placed on top of an Al coated pyrex mirror. This was done to simulate the metallization of the back side of the sapphire wafer which is often done in the LED industry. The Al coated mirror has the same reflectivity response that was shown in Figure 3-21. The sample “LED w/ 6 period DBR” refers to the LED grown on the 6 period DBR and likewise, the sample “LED w/ 12 period DBR” refers to the LED grown on the 12 period DBR. The mirror and the six period DBR had approximately the same effect and significantly increase the light emitted from the top of the device. Using a 12 period DBR increase the extracted light by approximately 25% more.

In summary, digital alloy based DBRs have been demonstrated a suitable substrate for LED growth. The quality, composition, and periodicity of the In$_x$Ga$_{1-x}$N/GaN based LEDs
Figure 6-7 – EL spectra of LEDs with and without integrated DBRs.

Figure 6-8 – Normalized LED intensity extracted from optical images.
grown on DBRs were similar to those grown on GaN epilayers. DBRs with reflectivity responses ranging from ~60% to ~90% have been integrated with 440 nm emitters and the light extraction has been measured. A considerable increase in light extraction has been measured from the top of the devices as compared to planar LEDs.
CHAPTER 7 – CURVED LEDS

7.1 Introduction

Two areas of interest in LED development include engineering the radiation profile and increasing the efficiency. Depending on the LED application, including solid state lighting, full color displays, automotive lighting, and device lighting\(^\text{134}\), either narrow or broad radiation patterns are desirable. For example, in solid state lighting applications, broad patterns are used to create uniform lighting from most angles. However, for short distance fiber optic communications, narrow emission patterns are necessary to improve fiber coupling efficiency\(^\text{135}\).

Furthermore, despite commercial success, nitride based LEDs are still hindered by limited efficiency. Part of this inefficiency arises from limited light extraction as nitride based devices have a narrow light escape cone and part from limited internal quantum efficiency due to material defects and polarization effects\(^\text{134}\). As a result, considerable research efforts have focused on developing techniques to modify the emission pattern and improving the efficiency of nitride based LEDs including, but not limited to, micro-lens\(^\text{136,137}\), photonic crystals\(^\text{134,138}\), resonant cavity devices\(^\text{135}\), and three-dimensional faceted structures\(^\text{139,140}\). Three-dimensional structures fabricated via Selective Area Growth (SAG) have been investigated for many years as a means for growing on semi-polar planes\(^\text{139,140}\). It has been well documented that growth on semi-polar planes reduce the internal electric field resulting from the spontaneous and piezoelectric polarization inherent in III-Nitride materials and is often pursued for the fabrication of high efficiency devices\(^\text{139,141}\).

In this work, the feasibility of fabricating emitters on curved surfaces has been demonstrated. The growth kinetics of the SAG of GaN have been utilized to fabricate curved surfaces. Indium Gallium Nitride (In\(_{1-x}\)Ga\(_x\)N) based Multi-Quantum Well (MQW) LED structures have been grown on the curved surface and devices have been fabricated using standard micro-fabrication techniques\(^\text{142}\). The electrical and optical characteristics of these structures are discussed. This technique present the potential for both engineering the emission pattern and also fabrication of devices on semi-polar planes.
7.2 Experimental Methods

All materials were grown via MOVPE in the Aixtron horizontal reactor. TMGa, TMAI, TMIn, TEGa, Cp₂Mg, and NH₃ were used as the precursors. Approximately 2 μm GaN buffer layers were grown on c-plane sapphire substrates using an AlN buffer layer. Silicon oxide and silicon nitride masks were deposited via Plasma Enhanced Chemical Vapor Deposition (PECVD) using an Oxford Plasma Lab 80 Plus system. Standard ultra violet photolithography techniques were used to create a striped pattern along \(<1\overline{1}00>\) direction of the GaN. This direction was used as the geometry of SAG with stripes along this direction is easily controlled by the growth conditions\(^{143}\). This striped pattern had window openings ranging from 80 μm – 110 μm with a spacing of 400 μm. The photoresist pattern was transferred to the dielectric mask by wet etching in buffered oxide etchant. The patterned substrate, as shown in Figure 7-1, was then returned to the MOVPE reactor for SAG. The surface kinetics on the dielectric materials prevent the deposition of GaN on the mask whereas the growth in the expose stripe is allowed\(^{143,144}\). As described previously for SAG and ELOG, the species on the surface of the mask will diffuse in the gas phase or migrate on the surface of the mask to the opening. By changing the mask dimensions there is localized engineering of the V/III ratio during growth. In this work, relatively large stripe widths and window openings were used such that the species diffusing from the mask would be incorporated along the edge of the stripe as compared to the center area giving rise to a curved cross section\(^{144}\) along the stripe as illustrated in Figure 7-1b. The SAG was performed at a reactor temperature of 1000 °C and pressure of 200 mbar. The TMGa flow rate was 120 μmol/min and V/III ratios of 560 and 940 were investigated. The dielectric mask was then removed by wet etching and a simple LED structure was grown. This included five InₓGa₁₋ₓN/GaN multi-quantum wells (MQWs) followed by a 10 nm \(p\)-AlₓGa₁₋ₓN layer and a 70 nm \(p\)-GaN layer. The TMIn flow rate for the MQW was 6 μmol/min, TEGa was 21 μmol/min, and the V/III ratio was 4800. The \(p\)-GaN was grown at 950 °C using 66 μmol/min of TMGa, 1.2 μmol/min of Cp₂Mg, and a V/III ratio of 1000. The most basic LED structure was targeted in this work in order to eliminate the challenges associated with growth on a non-planar surface.

In order to fabricate the LED structure, the flow process shown in Figure 7-2 was followed. First a circular \(p\)-type contact was deposited and then the LED mesa was etched as
Figure 7-1 – (a) Dielectric mask used for SAG. Stripes oriented along $<1\bar{1}00>$ direction. (b) SAG of GaN showing curved cross section. (c) LED growth on SAG of GaN.

Figure 7-2 – (a) Process flow for p-type contact deposition and mesa etch. (b) Process flow for n-type contact deposition.
shown in Figure 7-2a. The p-type contact consisted of 1nm Ni/150 nm Ag /50 nm Pt /20 nm Ni/50 nm Au deposited via e-beam evaporation in a Temescal BJD-2000 system. The LED mesa, approximately 100 μm in diameter and 400 nm thick, was etched using a Trion MiniLock III ICP Reactive Ion Etcher. The ICP etch leaves several mesas along the length of the stripe and exposes the underlying n-type GaN. A circular n-type contact was then deposited beside the mesa using e-beam evaporation. This contact consisted of 2 nm Ti/100 nm Al/30 nm Ti/200 nm of Au. SEM images were taken to evaluate the curved structure and also the fabrication process. Standard I-V characteristics and EL measurements were taken and compared to the planar LEDs grown under the same conditions.

7.3 Characterization

Figure 7-3 contains the SEM cross-sectional images of the SAG of GaN grown on the silicon nitride mask. The cross sectional image shows a curved center with a flat terrace along the edge of the stripe. The mask exhibited selectivity however there was minor parasitic deposition observed in between the stripes, which is most likely due to the large width of the dielectric stripe used in this work. The V/III ratio has previously been reported to directly influence the formation of defects along the edge of the stripe, however relatively smooth facets were observed using the growth conditions outlined above. Various V/III ratios and stripe dimensions were investigated to determine the effect on the growth of the curved structure. The V/III ratio and also the fill factor, defined as the window width/period, also affect the curvature

![Cross Sectional SEM of SAG of GaN using silicon nitride mask.](image)
of the SAG of GaN. The curvature was fitted as part of a larger circle and is shown in Figure 7-4. The mask material had an influence on the measured angle and is most likely due to a different sticking coefficient on the different dielectric masks leading to a different diffusion of the species to the growth site. Despite the mask material, the central angle decreased with increasing fill factor. In this work, the increase in fill factor corresponds to the increase in window width. The V/III ratio was also found to have a significant impact on the curvature of the SAG. Using these two factors, the angle of emission and also the planes of the SAG can be engineered.

Figure 7-5 contains an SEM image of the fabricated device. The circular $p$-type contact and mesa, as well as the circular $n$-type contact are shown.

Figure 7-6 contains the I-V characteristics as well as the electroluminescence of the LED on the curved GaN. The I-V measurements show high resistance most likely due to the large distances between the $n$-type and $p$-type contacts used in this work. The EL spectrum contains both the emission from the curved LED as well as from a planar device grown under the same conditions. The EL measurement from the curved LEDs exhibit a slightly broader emission spectra when compared to the equivalent planar structures, however it is centered at a similar wavelength. Since the dielectric mask was removed via wet etching prior to the LED growth, there is no additional contribution of gas phase diffusion of species from the mask to the growth site as is commonly observed in the SAG of In$_x$Ga$_{1-x}$N MQWs$^{12}$. The broader emission may therefore be due to a difference in growth rate and/or indium incorporation along the different facets that compose the curved structure. It has been well established that in III-Nitride materials, the different planes have varying degrees of stability and can influence the indium incorporation and quantum well thickness$^{13}$. The resources to accurately measure these features on the curved structure are not currently available. Future work will focus on optimizing the growth conditions for individual layers on the curved surface, at which time TEM will provide more valuable information.
Figure 7-4 – (a) Fitting points along SAG structure as circle. (b) Relationship between the central angle of the fitted circle to the fill factor and V/III ratio.
In summary, this work has demonstrated the growth and fabrication of III-Nitride based LEDs on curved GaN structures fabricated through SAG via MOVPE. A striped pattern along the $<1\overline{1}00>$ GaN direction with window widths ranging from 80-110 $\mu$m was used for the SAG. Silicon nitride and silicon oxide was used as the mask materials and exhibited good selectivity under the growth conditions employed, however minor parasitic deposition in the mask area was observed. Simple five period In$_x$Ga$_{1-x}$N/GaN MQW based LED structures were grown on the curved GaN stripes and devices were fabricated along the length of the stripe using standard fabrication procedures. The emission from the curved LED is slightly broader when compared to a planar device grown under the same conditions and may be due to the different growth kinetics along the different facets that compose the curved surface. This work demonstrates the ability to fabricate LEDs on curved surfaces and has potential applications in engineering the emission pattern and spectrum of nitride based LEDs by utilizing growth kinetics.

Figure 7-5 – SEM image of fabricated LED on curved GaN SAG.
Figure 7-6 – (a) Current-Voltage characteristics of curved LED and (b) electroluminescence spectra of curved and planar LED.
CHAPTER 8 – SELECTIVE AREA GROWTH AND EPITAIXIAL LATERAL OVERGROWTH

8.1 Introduction

As described previously, the Epitaxial Lateral Overgrowth (ELOG) is one common technique used to reduce the dislocation density reaching the surface of the epilayer. In this work, the ELOG process has been established using the MOVPE tool at WVU. As the ELOG process relies heavily on the Selective Area Growth (SAG) of GaN, it has been investigated both with and without rotation\cite{145,146,147} in order to develop a deeper understanding of the mechanisms governing this process and for three dimensional device geometries.

8.2 Selective Area Growth

During SAG of GaN, a dielectric mask is deposited on a GaN epilayer. The mask region consists of features on the order of 5-10 μm with exposed regions of the underlying GaN. When growth is conducted under certain conditions on this patterned substrate, the precursors will diffuse over the dielectric mask to the exposed GaN at which point they will be incorporated into the growth. Therefore, unique geometries can be achieved through the mask design and also through the growth conditions. In this work, a striped silicon dioxide mask was used as shown in Figure 8-1. An approximately 1.5μm thick GaN film was grown on a two inch diameter c-plane sapphire wafer via MOVPE using a two step process involving a ~30 nm AlN nucleation layer followed by

![Figure 8-1 – (a) Striped pattern along GaN <1̅100> direction](image)

\textit{Figure 8-1 – (a) Striped pattern along GaN <1̅100> direction}
a high temperature GaN main layer. TMGa and NH\textsubscript{3} were used as the source gasses while hydrogen and nitrogen were used as the carrier gases. Next, a 130 nm layer of silicon dioxide was deposited via Plasma Enhanced Chemical Vapor Deposition (PECVD) by an Oxford Plasma Lab 80 Plus system. Standard photolithography and wet etching techniques were used to create a striped pattern in the silicon dioxide layer along the GaN \textlangle 1\bar{1}00 \textrangle direction. The SAG of GaN was performed at different temperatures and V/III ratios. The window opening and the stripe width for these samples were both 5μm. Figure 8-2 shows cross sectional Scanning Electron Microscopy (SEM) images of SAG illustrating the control over cross sectional geometry that can be achieved by tailoring the growth conditions. Consistent with literature reports, growth at a low temperature (image a) resulted in slow growth rate and poor surface morphology. Increasing the

![Figure 8-2](image-url)
temperature as shown in image c results in the triangular cross section suitable for ELOG. In this figure, the second column (images b and d) are the results after re-growth on samples shown in the first column (images a and c) at a higher temperature and V/III ratio. Images b and d show the evolution of the faster lateral growth rate with increasing temperature and V/III ratio. Figure 8-3 shows the relationship between temperature and the ratio of lateral to vertical growth rate. As the temperature is increased, there is an increase in growth rate up to the point that species desorption from the surface results in a decrease in growth rate. These unique geometries have potential applications in device design. Additionally, since the facets of the three-dimensional planes are semi or non-polar planes, they are often pursued for device fabrication.

8.3 Epitaxial Lateral Overgrowth

As discussed previously, the triangular cross section is critical for the two step ELOG process in order to induce the dislocation bending. Based on the results of the SAG of GaN discussed, conditions were identified to fabrication structures with the triangular cross section as shown in Figure 8-2c. In order to consistently obtain triangular structures, the dielectric mask used was modified to have a 3 μm window width and a 7 μm stripe width. There are several different approaches that have been reported for the second step of the ELOG process in order to induce the lateral overgrowth. In this work, a high growth temperature was used. Temperatures 55-100 degrees higher than those used in the first step of the process were targeted. Figure 8-4 shows the cross sectional SEM images of the second step of the ELOG process conducted as different temperatures. Each sample shown was grown for the same amount of time. It should be noted that the temperatures reported are the nominal MOVPE temperatures used during growth. The system temperature was recalibrated between the time the SEM images in Figure 8-2 and Figure 8-4 were taken and is the cause for the apparent discrepancy in temperatures. After calibration, the temperature used to achieve the triangular cross section was approximately 960 °C. As can be seen in Figure 8-4, the performing the second ELOG step at 1015 °C was not sufficient to enhance lateral growth enough to achieve full coalescence. Increasing the temperature to 1040 °C as shown in image b resulted in full coalescence and a smooth surface morphology. Increasing the temperature even further, as shown in image c, results in coalescence however, the process was not as uniform over the entire sample compared to that achieved at 1040°C. Therefore, the growth
Figure 8-3 – Ratio of lateral to vertical growth rate as a function of growth temperature.

Figure 8-4 – Second step of ELOG at different growth temperatures. a) 1015°C b) 1040°C and c) 1065°C
temperature used in image b was chosen as the ideal temperature. Figure 8-5 contains two AFM images of the surface of the two step ELOG process. As can be seen in 1 μm x 1 μm scan, the surface shows the atomic steps as described previously. The RMS roughness for the 5 μm x 5 μm scan is 0.77 nm indicating a very smooth surface.

### 8.4 Selective Area Growth with No Rotation

In order to investigate the effects of gas flow on the SAG of GaN, two similarly sized pieces patterned with the SiO$_2$ mask as discussed in the previous section, were placed side by side in a line normal to the incoming gas flow in the reactor for re-growth. One sample was
placed with the striped pattern oriented parallel to the incoming gas flow and the other with the striped pattern oriented normal to the incoming gas flow as shown in Figure 8-6. In roughly half of the growths, the samples with the striped pattern oriented parallel to the incoming gas flow were located on the left hand side of the incoming gas, as shown in Figure 8-6. In the other growths, the sample with the striped pattern oriented parallel to the incoming gas flow was located on the right hand side. This configuration was used in order to simultaneously fit two samples on the two inch susceptor and ensure similar growth conditions by eliminating the effects of uneven gas flow and substrate heating. The samples were grown with no rotation at 1115°C, 200mbar, and a V/III ratio around 1400. The ammonia and the carrier gas flow rates were around 1.2 slm and 4.0 slm, respectively. The cross sections of the resulting three dimensional structures were observed using a Hitachi S4700 Scanning Electron Microscope (SEM) and conventional image processing techniques were used to determine the size of the structures. For practical reasons, one piece per growth per orientation was evaluated and each piece characterized was about one quarter of the size of the grown sample. In order to eliminate experimental artifacts in the analysis of the results, such as temperature non-uniformity, images and measurements were taken from areas of the two samples that were closest during growth. Any variations in the sample are summarized by the error bars shown in each figure.

Figure 8-6 – (a) Striped pattern along GaN \(<\bar{1}00>\) direction(b) Samples oriented parallel and normal to the incoming gas flow during selective area growth of GaN.
Occasionally, pieces from other parts of the sample, both off center and further downstream were evaluated. Based on SEM images, the growth results were consistent across the samples and fall within the shown error bars. Figure 8-7 and Figure 8-8 show the SEM images of the cross section for the samples with stripes oriented parallel and normal to the incoming gas flow. After 30 minutes of growth both orientations exhibited similar growth modes resulting in the same trapezoidal structure with the (0001), \{11\overline{2}2\}, and \{11\overline{2}0\} facets exposed shown in Figure 8-7. Therefore, all subsequent growths were performed on this base structure and the growth rates were measured as an increase in size from this base structure. It should be noted that no asymmetry in the structures was observed for either orientation as might be expected. Throughout later growth stages, both samples maintained a similar trapezoidal cross section with (0001), and \{11\overline{2}n\} facets \(n \approx 1.7 - 2.2\) exposed. The samples oriented normal to the flow experienced larger growth rates, as is indicated in Figure 8-9 and Figure 8-10.

Figure 8-10 illustrates the height as a function of growth time. Clearly, the normally oriented sample experiences a larger vertical growth rate when compared to the parallel oriented

Figure 8-7 – SEM image of SAG base structure. Structure was identical for samples with stripes oriented normal or parallel to incoming gas flow.
Figure 8-8 – SEM images of selective area growth of GaN with <1100> striped pattern oriented parallel and normal to the incoming gas flow.

sample. Figure 8-9 illustrates the cross sectional area of the three dimensional structures per pattern period. Once again, the normally oriented sample is displaying an enhanced cross sectional growth rate. In both Figure 8-9 and Figure 8-10, the line is a linear fit using the error bars as weight. Also shown in Figure 8-9 and Figure 8-10 is the height and cross section of the structures when SAG is carried out for 120 minutes while rotating the sample. The height of the rotating sample is similar to that of the normal oriented sample, while the cross section is similar to the parallel oriented sample. Since the growth conditions are identical for both orientations, the increased cross sectional growth rate of the normal oriented sample indicates that more species are being incorporated into the growth. Both orientations maintain exposed (0001), and \{1 \bar{1} 2 \bar{n}\} facets ($n \approx 1.7 - 2.2$) indicating that there is no difference in surface growth kinetics between the samples. In MOVPE, it is assumed that the gallium species sticking coefficient is unity on the GaN surface\(^{36}\) and there is efficient lateral transport across the oxide mask allowing all reactants reaching the surface to be incorporated into the SAG growth\(^{148,149}\). Therefore, it can be concluded that there is a difference in mass transport between the normal and parallel oriented sample to account for the difference in growth rate. The normal orientation experienced
enhanced growth rate because more species were diffusing to the growth site. The size of the structures is negligible when compared to the boundary layer thickness, which is proportional to the square root of the kinematic viscosity of the gas and the distance from the edge of the susceptor and inversely proportional to the square root of the free stream gas velocity\textsuperscript{26,150}. Therefore, it can be assumed that the height of the structure does not have a significant impact on the boundary layer, which is consistent with the fact that both orientations maintain constant growth rates even as the structures grow taller. These findings are contrary to the results obtained when the effect of gas flow was investigated on cubic GaN\textsuperscript{151}. Cubic GaN was reported to demonstrate only a slight dependence of the cross sectional geometry and growth rates on the orientation of the pattern with respect to the incoming gas flow. Future work is needed to further explain the mechanism responsible for dependence of growth rates on the orientation of the striped pattern with respect to the incoming gas flow.

\begin{figure}[h]
\centering
\includegraphics[width=0.8\textwidth]{figure8-9.png}
\caption{Cross section as a function of time for normal and parallel oriented samples. The lines are a linear fit of the data using the error bars as weight. The y-axis intercept corresponds to the cross sectional area of the base structure.}
\end{figure}
Figure 8-10 – Height as a function of growth time for parallel and normal oriented samples. The lines are a linear fit of the data using the error bars as weight. The y-axis intercept corresponds to the height of the base structure.
CHAPTER 9 – ALUMINUM NITRIDE SUSPENDED STRUCTURES

9.1 Introduction

AlN is often integrated with MEMS devices utilizing suspended structures, such as micro-bridges or cantilevers, for use as resonators, transducers, or RF switches. In general, the fabrication of suspended structures is complicated by the selection and removal of the sacrificial layer with respect to the functional material and substrate. Much attention has been focused on sputtered AlN due to its low cost and deposition temperature. Since it is widely known that the crystalline quality of AlN strongly affects the etch rate in certain acids and sputtered films are polycrystalline in nature, they often require a protective and supportive layer such as Silicon Nitride (Si$_3$N$_4$) or polysilicon in order to protect the functional material during the removal of the sacrificial layer and also provide the suspended framework.

More recently, growth techniques, such as MOVPE and MBE, have been employed for the fabrication of nitride based suspended structures. In these techniques, the sacrificial layer must serve as a pseudo-substrate for subsequent growth, but must also be able to be selectively removed. Most techniques have specifically targeted the fabrication of Gallium Nitride (GaN) based MEMS and NEMS structures by employing polycrystalline, epitaxial III-Nitride alloy, and recently nanocrystalline films as sacrificial layers. The fabrication techniques are relatively complex due to the high dependency on the material properties of the functional and sacrificial layers (e.g. bandgap, polarity, crystallinity, etc). Furthermore, the sapphire substrates used in these techniques limits the integration with current silicon MEMS technology.

This work presents a growth based fabrication technique for achieving unsupported suspended AlN structures grown via MOVPE using Silicon Oxide (SiO$_x$) sacrificial layers. AlN grown via MOVPE exhibits sufficient mechanical and chemical strength to maintain a suspended structure without a support layer and additionally withstands the selective removal of sacrificial oxide layers via wet etching. Therefore, the presented technique facilitates the fabrication of robust AlN suspended structures that are well suited for achieving group III-Nitride heteroepitaxial MEMS/NEMS systems on silicon substrates.
9.2 Experimental Method

Figure 9-1 illustrates the process flow for the fabrication. First the sacrificial layer was formed by depositing a SiO$_x$ ($x < 2$) film on silicon (111) substrates via Plasma Enhanced Chemical Vapor Deposition (PECVD) using an Oxford Plasma Lab 80 Plus system. The thickness of the sacrificial layer was varied from 200 nm to 800 nm. SiO$_x$ is often used as a sacrificial layer in surface micro-machining because it can be selectively etched with respect to silicon\textsuperscript{166}. Standard Ultra Violet (UV) photolithography and wet etching techniques were used to create a striped pattern in the oxide layer. The width of the stripes ranged from 5 µm to 110 µm and were oriented along the <110> direction. Other stripe orientations were also investigated but no significant differences were observed. This work targeted structures with various dimensions in order to verify the structural stability. AlN films were grown on the patterned substrates in an

![Figure 9-1 – Flow process for the fabrication of AlN suspended structures.](image-url)
Aixtron MOVPE horizontal reactor. Trimethylaluminum and ammonia were used as the precursors with typical flows around $12 \mu\text{mol/min}$ and $1.5 \text{ slm}$ respectively. Hydrogen was used as the carrier gas with the total flow into the reactor being approximately $9000 \text{ slm}$. All growths were conducted at a reactor pressure of $50 \text{ mbar}$ and a temperature around $1100 ^\circ\text{C}$ resulting in an approximate growth rate of $4 \text{ nm/min}$. For each sample, the film thickness was approximately equal to the sacrificial layer thickness, ranging between $250 \text{ nm}$ to $800 \text{ nm}$. Finally, the wafer was diced into smaller pieces by cutting the sample perpendicular to the stripes. The pieces were etched in a 10:1 Buffered Oxide Etch (BOE) to remove the oxide and dried. It is widely known that the crystalline quality affects the etch rate of AlN in various acids\(^1\),\(^2\),\(^3\). Since crystalline AlN has been reported to be resistant to attack by hydrofluoric acid\(^4\),\(^5\),\(^6\) and hydrofluoric acid is known to etch SiO\(_x\) uniformly selective to silicon, BOE was used in this work. It should be noted that etch rate, however, depends strongly on the method of SiO\(_x\) deposition due to variations in stoichiometry, density, and impurities\(^7\). SiO\(_2\) deposited via PECVD has a reported etch rate in BOE between $120 - 200 \text{ nm/min}$\(^8\). As this work used SiO\(_x\) ($x < 2$) deposited via PECVD, it is expected that the etch rate of the sacrificial layer is faster, but it has not been determined yet.

### 9.3 Characterization

The samples were analyzed in a Hitachi S4700 Scanning Electron Microscope (SEM) in order to extract structural information. Figure 9-2 shows SEM images of the AlN film grown over a $5 \mu\text{m}$ wide SiO\(_x\) stripe and the corresponding suspended structure after wet etching. The air gap has a height of approximately $200 \text{ nm}$ is and equal to the thickness of the sacrificial layer while the width is equal to the feature size on the lithographic mask used. Figure 9-3 shows a variety of suspended structures of different dimensions. Figure 9-3a contains an array of $5 \mu\text{m}$ wide structures with an $5 \mu\text{m}$ spacing. Fig. 3b shows the corners of the suspended structure in Figure 9-3a in more detail. Figure 9-3c illustrates a $24 \mu\text{m}$ wide structure having an air gap of $600 \text{ nm}$ while Figure 9-3d is a $90 \mu\text{m}$ wide structure with an air gap of $500 \text{ nm}$. The structures in Figure 9-3c and Figure 9-3d have low angled sidewalls when compared to those shown in Figure 9-3a and Figure 9-3b due to the combined effect of the thicker SiO\(_x\) layer and wet etching process prior to the AlN growth. Despite minor differences such as these, similar results were obtained for the various combinations of feature sizes and sacrificial layer thicknesses.
Figure 9-2 – (a) AlN grown on 5 μm wide SiO₂ stripe with 200 nm thickness (b) Free standing AlN micro-structures after removal of SiO₂ sacrificial layer. Air gap height: 200nm, AlN thickness: 250nm, Width: 5μm.

Figure 9-3 – (a) Array of 5 μm wide AlN micro-structures. Air gap height: 200 nm and AlN thickness: 250 nm, (b) Closer view of corner of AlN micro-structure in (a), (c) AlN micro-structure, air gap height: 500nm, AlN thickness: 500nm, width: 24 μm. (d) AlN micro-structure, air gap height: 600nm, AlN thickness: 500nm, width: 90 μm
Dispersive X-Ray Spectroscopy (EDS) was used to investigate the removal of the oxide layer under the AlN layer. Measurements taken several microns from the edge of a fresh cut did not indicate any underlying oxide.

These results demonstrate the growth and fabrication of AlN micro-structures using MOVPE and silicon surface micro-machining techniques. When grown via MOVPE, AlN nucleates easily on dielectrics, such as silicon oxide and silicon nitride, due to the short diffusion length and large sticking coefficient of the Al species\textsuperscript{169,43}. Therefore, AlN films grown on the sacrificial oxide layers are often of poor crystalline quality and can be removed by wet etching\textsuperscript{170}. Since it is known that for a given temperature, slower growth rates almost invariably produce higher quality films\textsuperscript{171}, this work targeted slow growth rates in order to promote surface diffusion and improve the crystal quality of the AlN on the oxide layer. Conditions used resulted in an AlN growth rate of 4 seconds per monolayer, which is significantly slower than typical nitride growth rates of 0.5 - 2.5 seconds per monolayer. It can therefore be speculated that the growth rate used in this work allows AlN grown on the SiO\textsubscript{x} surface to self-orient along the c-plane and therefore become etch resistant to BOE. The relationship between the quality of the AlN grown on the oxide layers and the growth conditions is currently being investigated.

In summary, a process for fabricating robust AlN micro-structures by combining silicon surface micromachining techniques and MOVPE growth has been demonstrated. Conditions were identified that result in the growth of etch-resistant polycrystalline AlN films on SiO\textsubscript{x} sacrificial layers. Etching techniques were developed to release the AlN from the sacrificial layer to fabricate unsupported, suspended structures for use as micro-bridges or channels. Due to the structural integrity of the material and simplicity of the fabrication process, complex, multi-level devices could be fabricated. Furthermore, the versatility of this technique lends itself to the fabrication of nano-sized suspended structures by reducing the thickness and width of the sacrificial layer, provided a suitable etchant is identified for complete removal of the sacrificial layer.
CHAPTER 10 – CONCLUSIONS AND FUTURE WORK

III-Nitride materials continue to attract attention for use in optoelectronic and electronic devices. With the ability to engineer the emission across the visible regime, this material system is of particular use for high brightness visible wavelength LEDs. Within LED fabrication, DBRs are required for increased light extraction as well for cavity based device such as RCLEDs and VCSELs. The fabrication of III-Nitride based DBRs has been hindered by material issues and growth challenges. In particular, the lattice and thermal mismatch with the III-Nitride system gives rise to defects and/or cracking when high refractive index contrast materials, such as AlN and GaN, are used in superlattice structures. In this work, two different strain relieving techniques have been investigated in order to eliminate cracking in high Al content Al_xGa_{1-x}N/GaN DBRs. In particular, indium was investigated as a surfactant and a digital alloy growth technique was developed and integrated in the DBRs for additional strain relief.

Indium has been reported to improve the surface morphology and crystalline properties of Al_xGa_{1-x}N alloys in addition to eliminating cracking in single layer films and MQW structures. In this work, it was used as a surfactant to minimize cracking in AlN/GaN DBRs. AlN/GaN DBRs have been fabricated and control over the center reflectivity wavelength was demonstrated by modifying the individual layer thickness. Trimethylindium was introduced during the growth of the AlN in the DBR and its influence on strain and cracking was investigated through evaluation of the surface crack density and also XRD measurements. Under optimal conditions, the surface cracking in a six period structure was reduced by a factor of two using indium during growth.

The second growth technique focused on developing a digital growth technique for Al_xGa_{1-x}N alloys. Digital alloy growth techniques utilize short period superlattice structures to alleviate the previously mentioned growth challenges including but not limited to, parasitic reactions of precursors, complex growth kinetics of multiple species, and phase separation. Furthermore, superlattice layers have long been used as strain relieving layers in layers to engineering the stress in III-Nitride layers. They have also been employed in AlN/GaN DBRs in order to suppress cracking. In this work, the digital alloy growth technique has been used to integrate a short period superlattice into each period of the DBR structure. This
allowed for a decrease in the total thickness of the DBR and complexity of the structure as compared to the superlattice insertion layers. Growth conditions which yield high quality Al\textsubscript{x}Ga\textsubscript{1-x}N digital alloys have been developed. The conditions determined maintain the superlattice structure as indicated by the presence of satellite peaks in the XRD measurements. Alloys with AlN mole fractions ranging from 0 to 1 have been grown by changing the AlN thickness within the superlattice. As these structures are crack-free, XRD measurements have been used to evaluate the difference in the strain evolution.

Digital alloys with an Al concentration around 90% have been integrated in Al\textsubscript{x}Ga\textsubscript{1-x}N/GaN DBR in place of the AlN layer. DBRs with up to 25 periods have been fabricated and exhibit ~94% reflectivity at the targeted wavelength. 2” substrates have been prepared and are largely crack-free with the only visible cracks lie along the edge of the 2” wafer. After a 600 nm overgrowth of GaN, these structures exhibit <1 nm surface roughness and are suitable for integration with active devices. In this work, LEDs have been grown on the DBR substrates and exhibit a considerable increase in light extraction as compared to a conventional LED on a GaN epilayer.

Future work includes integrating higher reflectivity DBRs with visible LEDs in order to increase the light extraction further. For the fabrication of RCLEDs, the use of a highly reflective top \textit{p}-contact or dielectric DBR will be integrated with the structure.

Other areas of interest for future work include doping studies on the digital alloys. There is a considerable interest in UV emitters for applications, including but not limited to, biological detection, water purification, sterilization, and medical diagnostics\textsuperscript{173}. Despite the recent improvement in efficiency of visible wavelength nitride-based emitters, the UV devices are still hindered by poor efficiencies and low output power. One of the challenges limiting efficiency includes the difficulty of achieving \textit{p}-type doping in high aluminum content Al\textsubscript{x}Ga\textsubscript{1-x}N alloys\textsuperscript{174}. Efficient doping becomes even more challenging in wider bandgap Al\textsubscript{x}Ga\textsubscript{1-x}N layers\textsuperscript{174} as the activation energy can exceed 300 meV for high Al concentrations\textsuperscript{175}. As a result, \textit{p}-type Al\textsubscript{x}Ga\textsubscript{1-x}N films are characterized by low carrier concentrations and mobilities which result in high device operating voltages and contact resistances\textsuperscript{176}. One technique that has shown promising results for overcoming the high activation energy is the use of Mg doped Al\textsubscript{x}Ga\textsubscript{1-x}N/GaN short period superlattice structures\textsuperscript{174,176}. The improvement in ionization efficiency results from the
valence band edge oscillations induced by the compositional modulation of the superlattice\textsuperscript{177}. This allows the acceptors to ionize where the band edge is below the Fermi level\textsuperscript{177} creating a sheet of holes parallel to the interface. While there have been many reports of enhanced carrier concentrations, it is typically for Al\textsubscript{x}Ga\textsubscript{1-x}N films with Al compositions of 0.40 or less\textsuperscript{175}. There are fewer reports extending this technique into high Al content alloys, x>0.50. Therefore it is of considerable interest to develop the doping techniques for digital alloys for integration in devices. Final areas of interest include integrating the digital alloys in electronic structures such as High Electron Mobility Transistors (HEMTs) as an alternative to random alloys.
APPENDIX A: TEMPERATURE CALIBRATION

The following steps can be used to calibrate the Luxtron temperature controller.

1. Under the conditions used for the pyrometer test, take the MOVPE temperature to 850°C.
2. Measure the temperature of the silicon (100) wafer with the infrared pyrometer from the top viewport on the reactor. Please note, the ceiling will have to be removed for the measurement.
3. If there is a discrepancy in the measured temperature and the temperature displayed in the Luxtron, then enter the Luxtron settings to reach the Sensor Calibration menu.
4. Reset the Sensor Calibration to the actual surface temperature that is measured with the pyrometer. ***Please note that if the setpoint and measured temperature differ significantly, use incremental values. For example, if the setpoint is 850 °C and the temperature measured with the pyrometer is 760 °C. The Sensor Calibration menu will initially display 850 °C. Change this to 825 °C. As the setpoint in the CACE software is still 850 °C the power will increase and heat the reactor until the Luxtron reads 850 °C. At this point, the reading with the pyrometer should increase 25 °C to 785 °C. Repeat this in increments of 25 ° until the pyrometer reading reaches 850 °C.
5. Note the sensitivity factors in the log book.
6. Perform a full pyrometer test to verify the temperature over a wide range of values.

The following settings should be used. The sensor factor may change with the above calibration process.

<table>
<thead>
<tr>
<th></th>
<th>Ch 1 (Si)</th>
<th>Ch 5 (InGaAs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sensor Type</td>
<td>Light Pipe</td>
<td>Light Pipe</td>
</tr>
<tr>
<td>Sensor Factor</td>
<td>3.729112E00</td>
<td>1.472112E00</td>
</tr>
<tr>
<td>Bandwidth</td>
<td>No Filter</td>
<td>Auto</td>
</tr>
<tr>
<td>Gain</td>
<td>Auto</td>
<td>Auto</td>
</tr>
<tr>
<td>Emissivity</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>Sensor Calibration</td>
<td>325</td>
<td>100</td>
</tr>
</tbody>
</table>

The output settings should be set to the following:

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Crossover</td>
<td>*</td>
</tr>
<tr>
<td>Temperature</td>
<td>600</td>
</tr>
<tr>
<td>Low Temperature Channel</td>
<td>5</td>
</tr>
<tr>
<td>High Temperature Channel</td>
<td>1</td>
</tr>
</tbody>
</table>
The output function should be set to the following:

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Low Temperature</td>
<td>100</td>
</tr>
<tr>
<td>Low Current</td>
<td>4 mA</td>
</tr>
<tr>
<td>High Temperature</td>
<td>1700</td>
</tr>
<tr>
<td>High Current</td>
<td>20 mA</td>
</tr>
</tbody>
</table>
This procedure is used to verify the susceptor temperature.

1. Remove the EpiRAS optics.
2. Remove the quartz ceiling from the reactor.
3. Load a clean Silicon (100) wafer.
4. Pump down the chamber to 200 mbar.
5. Set the flows to the following:

<table>
<thead>
<tr>
<th>MFC</th>
<th>Flow (sccm)</th>
<th>Gas</th>
</tr>
</thead>
<tbody>
<tr>
<td>Run Hydride</td>
<td>5</td>
<td>H2</td>
</tr>
<tr>
<td>Push Hydride</td>
<td>200</td>
<td>H2</td>
</tr>
<tr>
<td>Run MO</td>
<td>750</td>
<td>H2</td>
</tr>
<tr>
<td>Push MO</td>
<td>750</td>
<td>N2</td>
</tr>
<tr>
<td>Liner Purge</td>
<td>2500</td>
<td>H2</td>
</tr>
<tr>
<td>Rotation</td>
<td>200</td>
<td>N2</td>
</tr>
<tr>
<td>Window Purge</td>
<td>250</td>
<td>H2</td>
</tr>
<tr>
<td>Lightpipe</td>
<td>50</td>
<td>H2</td>
</tr>
</tbody>
</table>

6. Increase the temperature to 600 °C slowly.
7. Set the emissivity of the pyrometer to 0.69 and position so it is aimed through the top viewport of the reactor.
8. Measure the temperature with the pyrometer and compare to the setpoint over a wide range of temperature.
9. If necessary, adjust the sensitivity settings in the Luxtron controller as outlined in Appendix A.
APPENDIX C: MFC VERIFICATION

This procedure is used to verify the operation of the MFC.

1. Pump down to 0 mbar.
2. Open the throttle valve.
3. Close the Nitrogen supply.
4. Set Pressure Controllers to 0 and Mass Flow Controllers to maximum.
5. Wait until the base pressure has reached a minimum.
6. Close the throttle valve. The pressure should remain at the minimum.
7. Set the flow on the MFC under investigation to the desired value.
8. Open the gas supply to that MFC. The pressure in the reactor will start to increase.
9. Set the reactor pressure to 1000 mbar.
10. Note the rate at which the pressure increases.
11. Repeat steps 1-10 for a different MFC to compare.

Note: MFCs may be calibrated for different gasses.
%Read in image
close all, clear all;
files = dir('*.jpg');

for k = 1:length(files)

input_name = files(k).name;
[path name ext] = fileparts(input_name);

OImage = imread(files(k).name);
OImage = rgb2gray(OImage);
I2 = I2(50:500, 150:650);
I2 = ADAPTHISTEQ(I2);
level = graythresh(I2);
BWImage = im2bw(I2, level-.11);

BWImage = imcomplement(BWImage);
emptyrotate = zeros(size(BWImage));
newrotate = emptyrotate;
final = newrotate(1:length(newrotate(:,1)-1), :);

row = 1;
cnt = 0;
num = 20;
empty = 0;
enum = 7;

for angle = -90:1:90
BWrotate = imrotate(BWImage, angle);
newrotate = imrotate(emptyrotate, angle);

for row = 1:2:length(BWrotate(:,1))-3

cnt = 0;
empty = 0;
for col = 1:length(BWrotate(1,:))
    if BWrotate(row,col) == 1 || BWrotate(row+1,col) == 1
        cnt = cnt+1;
    else
        empty = empty +1;
        if empty>enum
            empty = 0;
            cnt = 0;
        end
    end
end
if (cnt > num)
    newrotate(row:row+1, col-(num+empty)+1:col) = 1;
    cnt = 0;
    empty = 0;
end
end
BWrotate = imrotate(BWImage, -1*angle);
newrotate = imrotate(newrotate, -1*angle);
l = floor(length(newrotate(1,:))/2);
w = floor(length(newrotate(:,1))/2);
l1 = floor((l-length(BWImage(1,:))/2))+2;
l2 = floor((l+length(BWImage(1,:))/2));
w1 = floor((w-length(BWImage(:,1))/2))+2;
w2 = floor((w+length(BWImage(:,1))/2));
newrotate = newrotate(w1:w2, l1:l2);

% add it to final
final = final(1:length(newrotate(:,1)),1:length(newrotate(1,:)));
final = final + newrotate;

end

for row = 1:1:length(final(:,1))
    for col = 1:1:length(final(1,:))
        if (final(row, col) > 0)
            final(row,col) = 1;
        end
    end
end

se = strel('disk',2);
erode = imerode(imcomplement(final), se);
[l num] = bwlabel(erode);
rgb = label2rgb(l);
sum_cols = sum(erode);
tot_sum = sum(sum_cols);
bwedge = edge(erode, 'sobel');
sum_cols = sum(bwedge);
tot_sum = sum(sum_cols);

output_name_edge = ['edge' name 'edge.tif'];
imwrite(bwedge, output_name_edge, '.tif');
saveto = ['edge' name 'edge.txt'];
save (saveto, 'tot_sum', '-ascii', '-append');
output_name_erode = ['erode' name 'erode' ext];
imwrite(erode, output_name_erode);
output_name_rgb = ['rgb' name 'rgb' ext];
imwrite(rgb, output_name_rgb);
saveto = ['counts' name '.txt'];
save (saveto, 'num', '-ascii', '-append');
saveto2 = ['countpix' name '_pix_sum.txt'];
save (saveto2, 'tot_sum', '-ascii', '-append');

end
FOR TE MODES:

clear all;
hold on;
%Design Wavelength
L = 470;
Lmicron = L/1000;
%Number of Bilayers
per = 18;
%Wavelength Range
L1 = 300;
L2 = 700;
per = 2*per;
n_gan = 2.1;
n_aln = 2.44;
n_air = 2.44;
dgan = L./(4*n_gan);
daln = L./(4*n_aln);
eta = 2.654*10^-3;
cnt = 1;
inc_angle = 0;
for inc_angle = 0:0.5:80
    inc_angle;
    theta_gan = (180/pi)*asin(n_air * sin(pi*inc_angle/180) / n_gan);
    for L = L1:1:L2
        Lmicron = L/1000;
        x = L + 1 - 300;
        BC = eye(2,2);
        k_o = 2*pi/L;
        for a = 1:1:per,
            if (mod(a,2)==1)
                n = n_gan;
                delta_r = ((dgan*2*pi*n)*(cos(pi*theta_gan/180)))/L;
                Y_i = n*eta*cos(pi*theta_gan/180);
            else
                n = n_aln;
                theta_aln = (180/pi)*asin(n_gan * sin(pi*theta_gan/180) / n_aln);
                n = n_aln;
        end
        theta_aln = (180/pi)*asin(n_gan * sin(pi*theta_gan/180) / n_gan);
    end
end
\[ \text{deltar} = \frac{(\text{daln} \times 2 \pi n) \times \cos(\pi \theta_{\text{aln}}/180)}{L}; \]

\[ \text{theta}_{\text{gan}} = \frac{(180/\pi) \times \sin(n_{\text{aln}} \times \sin(\pi \theta_{\text{aln}}/180))}{n_{\text{gan}}}; \]

\[ Y_i = n \times \eta \times \cos(\pi \theta_{\text{aln}}/180); \]

\[ \text{BC} = \text{BC} \times [\cos(\text{deltar}) \, , \, \text{i} \times \sin(\text{deltar})/Y_i \, , \, \text{i} \times Y_i \times \sin(\text{deltar}) \, , \, \cos(\text{deltar})]; \]

\[ Y_i = \frac{Z(2)}{Z(1)}; \]

\[ R(x) = \frac{(\eta \times \cos(\text{inc_angle} \times \pi/180) - Y)/(\eta \times \cos(\text{inc_angle} \times \pi/180) + Y)}{(\eta \times \cos(\text{inc_angle} \times \pi/180) - Y)/(\eta \times \cos(\text{inc_angle} \times \pi/180) + Y)}; \]

\[ \text{Reflect}(\text{cnt}) = R(160); \]

\[ \text{cnt} = \text{cnt} + 1; \]

\[ \text{ang} = 0:0.5:80; \]

\[ \text{plot}(\text{ang}, \text{Reflect}); \]

\[ \text{save}('\text{TE_FinalReflect460_inGaN.txt}', '\text{Reflect}', '-ascii'); \]

**FOR TM MODES:**

\[ \text{clear all;} \]

\[ \text{close all;} \]

\[ \text{hold on;} \]

%Design Wavelength

\[ L = 460; \]

\[ \text{Lmicron} = L/1000; \]

%Number of Bilayers

\[ \text{per} = 18; \]

%Wavelength Range

\[ L1 = 300; \]

\[ L2 = 700; \]

\[ \text{per} = 2 \times \text{per}; \]

\[ \text{n}_{\text{gan}} = 2.1; \]

\[ \text{n}_{\text{aln}} = 2.44; \]

\[ \text{n}_{\text{air}} = 2.44; \]

\[ \text{dgan} = L/(4 \times \text{n}_{\text{gan}}); \]

\[ \text{daln} = L/(4 \times \text{n}_{\text{aln}}); \]

\[ \eta = 2.654 \times 10^{-3}; \]

\[ \text{cnt} = 1; \]

\[ \text{inc_angle} = 0; \]

\[ \text{for inc_angle} = 0:1:80 \]
theta_gan = (180/pi)*asin(n_air * sin(pi*inc_angle/180) / n_gan);

for L = L1:1:L2
    Lmicron = L/1000;
    x = L + 1 - 300;
    BC = eye(2,2);
    k_o = 2*pi/L;
    for a = 1:1:per,
        if (mod(a,2)==1)
            n = n_gan;
            deltar = ((dgan*2*pi*n)*(cos(pi*theta_gan/180)))/L;
            Y_i = n*eta/cos(pi*theta_gan/180);
        else
            n = n_aln;
            theta_aln = (180/pi)*asin(n_gan * sin(pi*theta_gan/180) / n_aln);
            deltar = ((daln*2*pi*n)*(cos(pi*theta_aln/180)))/L;
            theta_gan = (180/pi)*asin(n_aln * sin(pi*theta_aln/180) / n_gan);
            Y_i = n*eta/cos(pi*theta_aln/180);
        end
        BC = BC*[cos(deltar) , i*sin(deltar)/Y_i ; i*Y_i*sin(deltar) ,
            cos(deltar)];
    end
    theta_sap = (180/pi)*asin(n_aln * sin(pi*theta_aln/180) / 1.78);
    Z = BC*[1 ; 1.78*eta/cos(pi*theta_gan/180)];
    Y = Z(2)/Z(1);
    R(x) = ((eta/cos(inc_angle*pi/180)-Y)/(eta/cos(inc_angle*pi/180)+Y))*((eta/cos(inc_angle*pi/180)-Y)/(eta/cos(inc_angle*pi/180)+Y))';
end

Reflect(cnt) = R(160);
cnt = cnt+1;
L = 300:1:700;
% plot(L,R)

Reflectivity_name = ['TM_inGaN_R_', num2str(inc_angle), '.txt'];
save('wavelength.txt', 'L', '-ascii');
save(Reflectivity_name, 'R', '-ascii');
end
ang = 0:1:80;
plot(ang, Reflect);
save('TM_FinalReflect460_inGaN.txt', 'Reflect', '-ascii');
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Peter Kozodoy, Monica Hansen, Steven P. DenBaars, and Umesh K. Mishra. “Enhanced Mg doping efficiency in Al0.2Ga0.8N/GaN superlattices,” Appl. Phys. Lett. 74, 3681 (1999).